IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
John W. Regnier

Application No.: 10/050,067   Group Art Unit: 2176

Filed: January 17, 2002   Examiner: Not Yet Assigned

For: A FAST ALGORITHM TO EXTRACT
      FLAT INFORMATION FROM
      HIERARCHICAL NETLISTS

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Technology Center 2100

Assistant Commissioner for Patents
Washington, D.C. 20231

DECLARATION OF JOHN W. REGNIER UNDER 37 CFR 1.132

Dear Sir:

I John W. Regnier declare and state as follows:

1. I reside at 2058 Crimson Rose Way, Boise, Idaho 83709.

2. I am the named inventor of the above-identified U.S. patent application ("the '503 application"), filed on January 17, 2002, as evidenced by the attached executed Declaration and Power of Attorney filed with the application (Exhibit 1).

3. I have examined and researched my records and the records at Micron Technology, Inc. the assignee of the application, for information which may be deemed material to patentability of the pending claims of the application.

4. The claimed invention of the present application relates to a flat data extraction system that extracts flat data from hierarchical lists (hereinafter, "flat data system"). The flat data system output is used as an input to an electrical rule check (ERC)

program. The ERC program scans the flat data system output and generates a chip design error report.

5. Exhibit 2 shows my revisions to the flat data system. As shown, my work on the flat data system began on November 16, 1998. (Exhibit 2, p. 7) Exhibit 2 shows 61 revisions made to the flat data system over the course of its development to a reliable error checking system.

6. Exhibit 3 shows a record of the runs of the flat data system on a variety of chip design schematics. As shown in Exhibit 3, in February of 1999, a number of different chip design engineers began to run the flat data system to evaluate its performance and design on a variety of chip schematics, including SRAMs, DRAMS and other parts which use different schematic syntax and symbols.

7. The Micron Technology, Inc., Research and Development (R&D) department was the first group to begin to use the flat data extraction system to extract flat data from an integrated circuit netlist for use as an input to an ERC program. The first use occurred on February 4th, 1999. (Exhibit 3, p. 1) Exhibit 3 in conjunction with Exhibit 2 shows that the use of the flat data system was intermittent, incomplete as to use on an entire schematic, and that the flat data system was used solely for testing purposes from February 1999 to May of 2001. For example, the run on February 4, 1999 generated experimental flat data output on a part under development (part Y84B). (Exhibit 3, p. 1) Exhibit 3 shows a single run of the flat data system rather than the minimum 10 or 20 runs which are required to adequately test a design over each iteration of design revision. (Exhibit 3, p. 1) The experimental flat data system output from this use, which was input into an electrical rule check program (ERC), generated numerous false indications of errors in the Y84B chip design. A design rule check program, the primary chip design error detection system, was also run at the same time on Part No. Y84B. Due to the continuing output of large number of false chip defect errors from the ERC program/flat data system, it was necessary to continue to use the DRC system as the primary testing system until the flat data system was determined to be reliable.

8. The next use of the flat data system was on March 1, 1999 on Part No. R85A, and involved only 15 runs on approximately twenty different cells in the design schematic out of approximately 1,000 different cells. (Exhibit 3, p. 1) This pattern

indicates, again, that the flat data system was being used to evaluate the ERC program experimental flat data output results, rather than actually being used or relied upon, in cooperation with the ERC system, to test an entire design. As before, the DRC program was run and relied upon for the testing of the R85A chip netlist design.

9. The flat data system was again run on design data for Part No. Y95C (Exhibit 3, p. 4). Again, however, the system was used in conjunction with the DRC program for the purpose of evaluating the flat data system. Another part, T85A, was also designed using the DRC program as the primary test system, with the flat data system inputs to an ERC program being run for evaluation and testing purposes. The lack of use of the flat data system as a primary error detection system for the T85A design is demonstrated by the fact that the T85A chip was first designed in late 1999, and yet, the flat data system was not run on the T85A schematic until October of 2000. (Exhibit 3, p. 7) Even then, the flat data system was not relied upon to produce the T85A chip, engineering samples of which were distributed under a confidentiality agreement in January of 2000 (10 months before the flat data system was run on the T85A schematic) and which were then sold in January 2001. The DRC program, rather than the flat data system/ERC program, was again used to provide error analysis that was relied upon for the T85A chip design. The flat data system was also run in the same manner described above, with the DRC program as the primary design error checking system, on a variety of other designs, including the SRAM designs of S78, S91A, S93A, and other designs such as D94B, E85A, H96A, R85A, R85B, X84B, Y15A, Y85Z, Y15B, Y16A, Y16X/Z or R96A (Exhibit 3). (None of these chips were distributed as samples, offered for sale or sold.)

10. All of the runs of the flat data system on circuit or part designs continued in the above-described manner until midway through the Y95W part development program. (Exhibit 3, p. 13) The Y95W part is the first part which was sold that used the flat data system to generate flat data and ERC testing results for the entire chip design schematic that was used and relied upon to produce a chip that was actually distributed or sold.

11. The flat data system was first run on the Y95W part netlist schematic on October 12, 2000 (Exhibit 3, p. 7) The decision to perform final production testing on the flat data system for use with the flat data system/ERC program, and thereby evaluate the usefulness of the flat data system, was made in November 2000. The first time the entire Y95W schematic was scanned by the flat data system was in November 2000 (Exhibit

3, p. 7). The circuit design engineers continued to run the pre-existing test system, the DRC program, on chip netlists as well as performing manual checks on the circuit. I met frequently with the design engineers to review the flat data program output from November 2000 through July 2001. Twenty seven modifications of the flat data system program were made during the Y95W part development through May 2001. (Exhibit 2, p. 1-2) No changes were made in June 2001 as the system was determined fully functional. (Exhibit 2, p. 2) The Y95W part was produced and samples distributed in October 2001. The first record of a sale was in January 2002.

12. In July 2001, the Micron R&D and Production Department made their decision as to the reliability and required use of the flat data system/ERC program. A decision was made at that time to eliminate the use of some of the previous primary error testing checks on circuit design elements, performed by the DRC system. After July 2001, the flat data system/ERC program was required to be run on all designs and especially before tape out. In fact, the first time that a system design went to tape-out after flat data system extraction/ERC checking without the redundant DRC checking was approximately in October 2001. The first shipment of a part which used the flat data system as a primary test system, the Y95W part, was made in January 2002.

13. All statements made herein of my own knowledge are true and all statements made on information and belief are believed to be true; and these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful and false statements may jeopardize the validity of the above-identified patent.

Date:_____3-11-03_____   By:_____John W. Regnier_____
                                             John W. Regnier

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EXHIBIT 1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

DECLARATION FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled:

A FAST ALGORITHM TO EXTRACT FLAT INFORMATION FROM HIERARCHICAL NETLISTS

the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by an amendment, if any, specifically referred to in this oath or declaration.

I acknowledge the duty to disclose all information known to me which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code § 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Not Claimed
			☐
(Number)	(Country)	(Filing Date)	
			☐
(Number)	(Country)	(Filing Date)	
			☐
(Number)	(Country)	(Filing Date)	

I hereby claim the benefit under Title 35, United States Code, § 120/365 of any United States and PCT international application(s) listed below and, insofar as

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the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code,

§ 112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56(a) which became available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application No.)	(Filing Date)	(Status) (patented, pending, abandoned)
(Application No.)	(Filing Date)	(Status) (patented, pending, abandoned)
(Application No.)	(Filing Date)	(Status) (patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Please address all correspondence to Thomas J. D'Amico of **Dickstein Shapiro Morin & Oshinsky LLP** located at 2101 L Street NW, Washington, DC 20037-1526. Telephone calls should be made to (202) 785-9700.
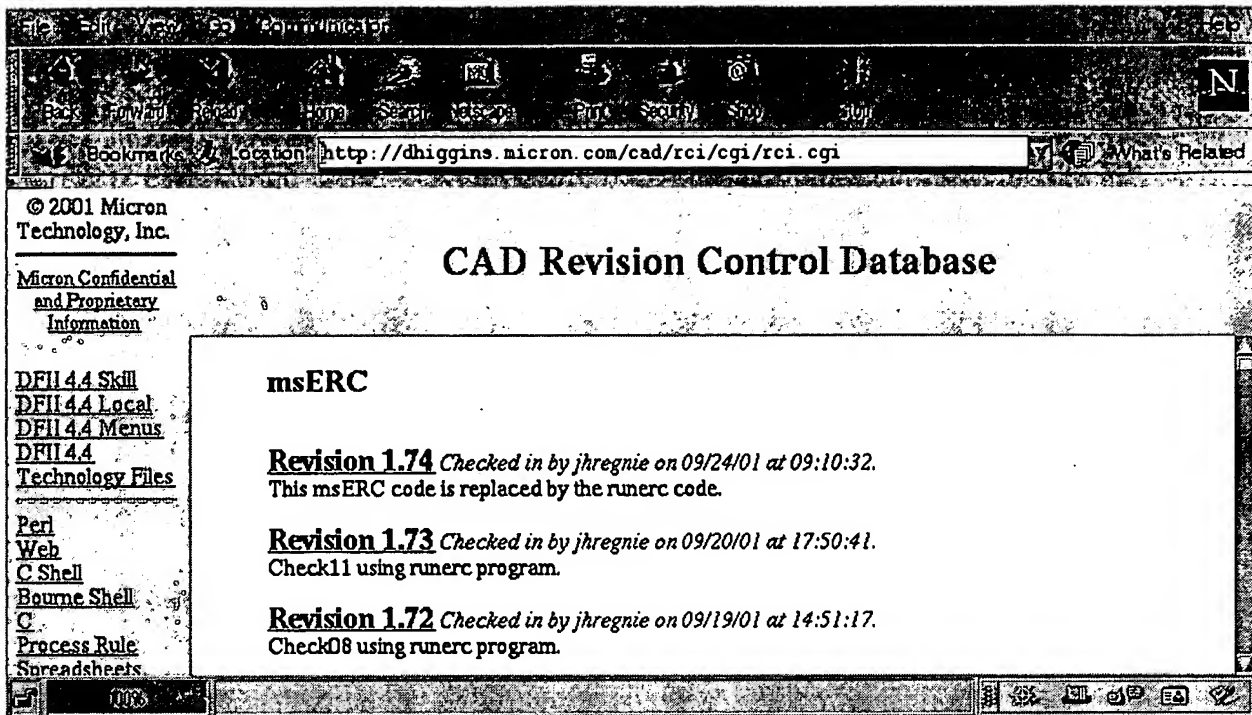
Full name of sole inventor: John W. Regnier

Inventor's signature: _John W. Regnier_   Date: 01-21-2002

Residence: Boise, Idaho

Citizenship:

Mailing Address: 2058 Crimson Rose Way
Boise, Idaho 83709

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EXHIBIT 2

CAD Revision Control Database

DFII 4.4 Skill
DFII 4.4 Local
DFII 4.4 Menus
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Technology Files

Perl
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msERC

Revision 1.74 *Checked in by jhregnie on 09/24/01 at 09:10:32.*
This msERC code is replaced by the runerc code.

Revision 1.73 *Checked in by jhregnie on 09/20/01 at 17:50:41.*
Check11 using runerc program.

Revision 1.72 *Checked in by jhregnie on 09/19/01 at 14:51:17.*
Check08 using runerc program.

Location: http://dhiggins.micron.com/cad/rci/cgi/rci.cgi

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EXHIBIT 2

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Revision 1.71 *Checked in by jhregnie on 09/13/01 at 14:08:16.*
Check07 using runerc program.

Revision 1.70 *Checked in by jhregnie on 09/12/01 at 13:59:07.*
Check06 using runerc program. Fixed Check05 so it is recorded in erc.out file.

Revision 1.69 *Checked in by jhregnie on 09/07/01 at 10:13:16.*
09/07/2001 – John Regnier Check09 using runerc program.

Revision 1.68 *Checked in by jhregnie on 09/05/01 at 14:44:51.*
Check05 using runerc program. Check12 using runerc program.

Revision 1.67 *Checked in by jhregnie on 08/27/01 at 09:26:42.*
Check04 using runerc program.

Revision 1.66 *Checked in by jhregnie on 08/23/01 at 08:40:04.*
Check03 using runerc program.

Revision 1.65 *Checked in by jhregnie on 08/14/01 at 09:28:19.*
Check02 using runerc program.

Revision 1.64 *Checked in by jhregnie on 08/09/01 at 11:05:28.*
For #11 Changed %INSTS to %INSTS_ORIGINAL for cell name lookup.

Revision 1.63 *Checked in by jhregnie on 08/02/01 at 10:32:54.*
Check01 using runerc program. Check for slib cells in checkMarkersAcrossMOS. Do not sort the
errors after removing redundant and masking. Check08 reports errors in different files for each marker.
Remove leading X from slib properties.

Revision 1.62 *Checked in by jhregnie on 07/09/01 at 15:37:40.*
Scientific notation is ok for L in checkSourceOrDrainOnRail.

Revision 1.61 *Checked in by jhregnie on 05/15/01 at 15:53:55.*
Check polarity of devices on SVAF nets.

Revision 1.60 *Checked in by jhregnie on 05/14/01 at 11:48:10.*
Fixed the markers across MOS check. Turned off 01_01 for now.

Revision 1.59 *Checked in by jhregnie on 05/08/01 at 10:48:07.*
Check for markers across MOS based on the gate.

Revision 1.58 *Checked in by jhregnie on 04/30/01 at 10:45:32.*
Pad check changed as DRC looks for esd now.

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Revision 1.58 Checked in by jhregnie on 04/30/01 at 10:45:52.
Pad check changed as DRC looks for esd now.

Revision 1.57 *Checked in by jhregnie on 04/25/01 at 11:28:41.*
SVAF marker added.

Revision 1.56 *Checked in by jhregnie on 04/19/01 at 16:09:55.*
Fixed typo in latchup check from @b to $b for bulk pin.

Revision 1.55 *Checked in by jhregnie on 04/18/01 at 15:33:47.*
When reading the rules check for /^_/ for overwrite else append. Check for thin PMOS gate as well as drain and source on P

Revision marked *Checked in by on //net. at .*

Revision 1.54 *Checked in by jhregnie on 04/17/01 at 09:28:25.*
Point to the proper runerc script.

Revision 1.53 *Checked in by jhregnie on 04/17/01 at 09:05:42.*
Fixed 02_01 check to not allow certain markers on the same net. Check for P

Revision marker *Checked in by and on //across at options.*
For pad check do not short options or resistors. For pad check do not check for esd devices. Run the new 'runerc' program and then the old msERC program.

Revision 1.52 *Checked in by jhregnie on 03/09/01 at 16:11:37.*
Changed floating gate check to jump part type options with PLUS/MINUS pins. Fixed 07_03 check for sram.

Revision 1.51 *Checked in by jhregnie on 01/10/01 at 22:55:23.*
Copy the P

Revision marker *Checked in by down on //up at and.*
the NV,HV,SV markers. Check for P

Revision marker *Checked in by hierarchy on //in at the.*

Revision 1.50 *Checked in by jhregnie on 01/04/01 at 15:29:05.*
Fixed copyCell routine to look for @UNDOMAPNODES for 'nip' update. Added the markerPriority rule for checking across devices. Check for NV marker across resistors.

Revision 1.49 *Checked in by jhregnie on 12/29/00 at 14:08:42.*
Check marked nets for thin mos bulk node. Check for latch up during power up as well as after. Add partTypeOptions to be checked for pad protection.

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partTypeOptions to be checked for pad protection.

Revision 1.48 *Checked in by jhregnie on 12/07/00 at 07:56:33.*
Floating ncap and pcap gate are not an error now. Don't check marked bulk node yet.

Revision 1.47 *Checked in by jhregnie on 12/06/00 at 14:12:15.*
Do not flag conflicting markers/rails through MOS transistors. Check marked bulk node for thin mos on marked net.

Revision 1.46 *Checked in by jhregnie on 11/30/00 at 09:37:46.*
Changed masking to account for larger mask rule than error being masked.

Revision 1.45 *Checked in by jhregnie on 11/28/00 at 12:32:01.*
Syntax error fix.

Revision 1.44 *Checked in by jhregnie on 11/28/00 at 10:54:36.*
Changed 4.2 to look for NV opposite vbb! for PMOS. Floating gates check shorts partTypeOptions for now.

Revision 1.43 *Checked in by jhregnie on 11/27/00 at 17:02:09.*
Added floatingGatePort rule so ports are not considered floating. Changed permissions for
.../rules/erc/masks directory to 775 from 755.

Revision 1.42 *Checked in by jhregnie on 11/14/00 at 12:42:45.*
Added the error 12 file declarations.

Revision 1.41 *Checked in by jhregnie on 11/14/00 at 09:56:08.*
Write 6 and 12 errors to different files.

Revision 1.40 *Checked in by jhregnie on 10/30/00 at 09:10:48.*
Added checkFloatingGates and changed @CELLS to %CELLS structure. Added the NV marker and modified most checks to accomodate it. Ignore L2POLY cells on marked P

Revision nets. *Checked in by on // at .*

Revision 1.39 *Checked in by jhregnie on 09/28/00 at 10:08:21.*
The checkHVNetPmosConnection looks through resistors now. If SRAM then HV and gnd! ok across mos devices.

Revision 1.38 *Checked in by jhregnie on 09/26/00 at 17:18:09.*
Only use @RAILSPWRG

Revision for *Checked in by SRAM on //undomapnodes. at If.*
gnd! net.

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Revision 1.37 *Checked in by jhregnie on 09/26/00 at 13:29:47.*
Find undomapnodes nets by matching start of line, add @RAILSPWRG

Revision too. *Checked in by on // at .*

Revision 1.36 *Checked in by jhregnie on 09/18/00 at 12:26:58.*
Changed to read raw netlist and then the merged netlist.

Revision 1.35 *Checked in by jhregnie on 09/07/00 at 10:35:19.*
Added check for conflicting markers in checkMarkersAcrossMOS and revised.

Revision 1.34 *Checked in by jhregnie on 08/07/00 at 15:33:41.*
Check for HV and SV across options, not HV or SV.

Revision 1.33 *Checked in by jhregnie on 07/21/00 at 14:47:04.*
Initial release...again.

Revision 1.32 *Checked in by jhregnie on 05/08/00 at 17:21:52.*
Don't check markers across /^R?MSRSW/ switches that were generated.

Revision 1.31 *Checked in by jhregnie on 05/01/00 at 11:04:55.*
Don't copy HV/SV for THIN through resistors onto pwr/gnd nets.

Revision 1.30 *Checked in by jhregnie on 04/25/00 at 14:24:44.*
Save the DIGIS of thin mos on pad nets.

Revision 1.29 *Checked in by jhregnie on 04/19/00 at 16:05:52.*
The P

Revision markers *Checked in by allowed on //are at not.*

Revision 1.28 *Checked in by jhregnie on 04/12/00 at 09:42:45.*
Added cn cell to not be a primitive for checkMarkingHierarchalNets.

Revision 1.27 *Checked in by jhregnie on 04/11/00 at 14:50:46.*
Check for HV,SV on Thin MOS through resistors as well. Only check for markers across resistors if SRAM.

Revision 1.26 *Checked in by jhregnie on 04/10/00 at 09:12:03.*
Fixed checkPadProtection to not check the @NOCHECKCELLS.

Revision 1.25 *Checked in by jhregnie on 03/29/00 at 10:31:36.*
Added rule @NOMARKEROKINCELLS to ignore missing marker in cells.

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Revision 1.24 *Checked in by jhregnie on 03/14/00 at 08:40:26.*
Change transistors netlisted as capacitors to be treated as transistors.

Revision 1.23 *Checked in by jhregnie on 02/29/00 at 13:46:28.*
Check for vbb! on THIN MOS transistors. The Base ERC Rules are now read from
/usr/micron/rules/erc/* directory.

Revision 1.22 *Checked in by jhregnie on 02/25/00 at 14:39:17.*
Changed checkPadProtection, web page reflects changes. Changed the default rules for
thick[PIN] Transistors.

Revision 1.21 *Checked in by jhregnie on 02/18/00 at 14:14:20.*
Don't check erc.rules for HV>non--HV and HV>SV because vssq! is HV and less priority as far as
latchup Priority is concerned.

Revision 1.20 *Checked in by jhregnie on 02/16/00 at 16:08:50.*
Check for marker across /^resistor$/ for DRAM as well as SRAM. Added @HVNETSTHIN KSDRC
but hardwired in this code and these nets do not require a HV marker. Changed
checkMarkingHierarchalNets to not require a HV marker for the @HVNETSTHIN KSDRC nets at all
matchPoint levels. Only check pmos not turned off if SRAM.

Revision 1.19 *Checked in by jhregnie on 02/15/00 at 13:56:24.*
Changed checkHVPmosConnection to not flag ports at current top level, and look across
partTypeOptions and NMOS with gate of HV or SV. Changed mask directories permissions to 775 form
755. Set the umask to 002.

Revision 1.18 *Checked in by jhregnie on 02/02/00 at 09:47:47.*
Added partTypeOptions rule for checking across devices.

Revision 1.17 *Checked in by jhregnie on 01/06/00 at 09:59:00.*
Changed to check for hierarchical markers only in matchPoint cells. The nets in hvNets do not need to
be marked.

Revision 1.16 *Checked in by jhregnie on 11/10/99 at 11:38:17.*
Added the cell path to the probes that are written out to files. Changed how remove redundant probes
and now masks errors. Copy the original probe files before removing masked errors. Added memusage
to appear in the output file. Changed checkSourceDrainOnRail so error if sdOnRailPChanLo always.

Revision 1.15 *Checked in by jhregnie on 08/26/99 at 15:18:14.*
Changed checkMarkersAcrossSwitches to allow different marker across.

Revision 1.14 *Checked in by jhregnie on 08/26/99 at 11:07:01.*
Added NAARES to the getPadDevices function.

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Location: http://dhiggins.micron.com/cad/rci/cgi/rci.cgi

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Revision 1.13 *Checked in by jhregnie on 08/25/99 at 14:11:21.*
Changed checkMarkersAcrossMOS MOS so marker may not be required. Only
checkHVNetPmosConnection if partType includes SRAM.

Revision 1.12 *Checked in by jhregnie on 08/24/99 at 17:21:46.*
Modified printVirtual to not print out all the nets. Short fuses when looking for HVNetPmosConnection.
Changed esd2Hi, esd2Lo, svBootGateNet rules to handle mutiple values. Added addMarkersToCells
rule to automatically add HV markers to cells. Added addMarkersToHVNets rule to add HV markers to
the HV nets.

Revision 1.11 *Checked in by jhregnie on 07/12/99 at 12:24:53.*
If SRAM add markers to all hvNets nets. If SRAM check markers across resistors.

Revision 1.10 *Checked in by jhregnie on 07/08/99 at 10:22:23.*
Added command line execution, exploding cells, thin transistors.

Revision 1.9 *Checked in by jhregnie on 04/27/99 at 11:20:10.*
Major changes to the SV Node checks.

Revision 1.8 *Checked in by jhregnie on 04/08/99 at 07:15:07.*
Major changes to the Pad Protection check.

Revision 1.7 *Checked in by jhregnie on 04/05/99 at 14:44:21.*

Revision 1.6 *Checked in by jhregnie on 03/18/99 at 12:28:24.*

Revision 1.5 *Checked in by jhregnie on 02/16/99 at 13:33:09.*
Changed the post processing of the files a bit. The rail nets (vcc,gnd...) are not added as hv or sv nets
and if the rail nets are hv or sv an error is reported that a conflict exists.

Revision 1.4 *Checked in by jhregnie on 02/02/99 at 12:30:37.*
If no rules then use a default set and print warning. Fixed findALowestPath function (again) to stop on a
global net instance. Added function logUsage to write usage to a log file.

Revision 1.3 *Checked in by jhregnie on 12/14/98 at 12:01:08.*
Added PDEFL for checking p transistors on hv/sv nets. Changed pad protection check.

Revision 1.2 *Checked in by jhregnie on 11/23/98 at 14:51:00.*
Fixed findALowestPath function. Added %MAP2NET for mapping the erc.rules to the netlist
representation.

Revision 1.1 *Checked in by jhregnie on 11/16/98 at 11:59:36.*
Initial Release

EXHIBIT 3

MsERC.log_11oct2001

```
Thu Feb  4 11:28:33 MST 1999---jcullum@rndcs16---/flu4/proj/y84b/REV/ercrun/wlrc2
Mon Mar  1 09:03:47 MST 1999---tmanning@rndcs10---/rlu9/proj/r85a/REV/ercrun/r85aQ
Mon Mar 22 11:17:22 MST 1999---cmartin@rndcs9---/rlu9/proj/r85a/REV/ercrun/colred_1bank
Mon Mar 22 11:24:57 MST 1999---cmartin@rndcs9---/rlu9/proj/r85a/REV/ercrun/colred_1bank
Mon Mar 22 11:31:56 MST 1999---cmartin@rndcs13---/rlu9/proj/r85a/REV/ercrun/MicronPeriph_top
Mon Mar 22 12:42:08 MST 1999---cmartin@rndcs8---/rlu9/proj/r85a/REV/ercrun/MicronPeriph_top
Mon Mar 22 14:08:49 MST 1999---cmartin@rndcs12---/rlu9/proj/r85a/REV/ercrun/array_16bnkDrvrs
Mon Mar 22 14:18:48 MST 1999---cmartin@rndcs18---/rlu9/proj/r85a/REV/ercrun/rdram_128M
Tue Mar 23 09:52:15 MST 1999---cmartin@rndcs19---/rlu9/proj/r85a/REV/ercrun/MicronPeriph_top
Tue Mar 23 10:03:14 MST 1999---cmartin@rndcs11---/rlu9/proj/r85a/REV/ercrun/MicronPeriph_top
Tue Mar 23 10:18:43 MST 1999---cmartin@rndcs11---/rlu9/proj/r85a/REV/ercrun/array_16bnkDrvrs
Tue Mar 23 10:39:00 MST 1999---cmartin@rndcs11---/rlu9/proj/r85a/REV/ercrun/array_16bnkDrvrs
Tue Mar 23 11:30:53 MST 1999---cmartin@rndcs18---/rlu9/proj/r85a/REV/ercrun/36MGarray
Tue Mar 23 14:56:20 MST 1999---cmartin@rndcs12---/rlu9/proj/r85a/REV/ercrun/MicronPeriph_bot
Tue Mar 23 15:16:09 MST 1999---cmartin@rndcs12---/rlu9/proj/r85a/REV/ercrun/r85aQ
Mon Apr  5 12:36:20 MDT 1999---tbard@rndcs5---/rlu9/proj/r85a/REV/ercrun/1Regs_hold
Mon Apr  5 12:45:36 MDT 1999---tmanning@rndcs10---/rlu9/proj/r85a/REV/ercrun/1DAmdeTim
Mon Apr  5 12:47:49 MDT 1999---tmanning@rndcs11---/rlu9/proj/r85a/REV/ercrun/1DAmdeTim
Mon Apr  5 12:51:27 MDT 1999---tmanning@rndcs12---/rlu9/proj/r85a/REV/ercrun/1DAmdeTim
Mon Apr  5 12:53:21 MDT 1999---tmanning@rndcs7---/rlu9/proj/r85a/REV/ercrun/1DAmdeTim
Mon Apr  5 12:57:31 MDT 1999---tmanning@rndcs12---/rlu9/proj/r85a/REV/ercrun/1DAmdeTim
Mon Apr  5 12:59:20 MDT 1999---tmanning@rndcs20---/rlu9/proj/r85a/REV/ercrun/1DAmdeTim
Mon Apr  5 13:01:10 MDT 1999---tmanning@rndcs10---/rlu9/proj/r85a/REV/ercrun/1DAmdeTim
Mon Apr  5 13:02:48 MDT 1999---tmanning@rndcs13---/rlu9/proj/r85a/REV/ercrun/1DAmdeTim
Mon Apr  5 14:51:51 MDT 1999---tmanning@rndcs6---/rlu9/proj/r85a/REV/ercrun/1DAmdeTim
Mon Apr  5 15:04:54 MDT 1999---cmartin@rndcs13---/rlu9/proj/r85a/REV/ercrun/r85aQ
Fri Apr 23 10:47:40 MDT 1999---tcowles@rndcs5---/rlu17/proj/y95a/REV/ercrun/cgndDrv
Fri Apr 23 10:52:43 MDT 1999---tcowles@rndcs13---/rlu17/proj/y95a/REV/ercrun/cgndDrv
Fri Apr 23 10:59:38 MDT 1999---tcowles@rndcs17---/rlu17/proj/y95a/REV/ercrun/cgndDrv
Fri Apr 23 11:06:22 MDT 1999---tcowles@rndcs17---/rlu17/proj/y95a/REV/ercrun/cgndDrv
Fri Apr 23 11:06:04 MDT 1999---tcowles@rndcs12---/rlu17/proj/y95a/REV/ercrun/cgndDrv
Mon May 17 11:11:33 MDT 1999---wfjones@rndcs5---/rlu2/proj/t84a/REV/ercrun/addrcmux3
Mon May 17 11:16:04 MDT 1999---wfjones@rndcs11---/rlu2/proj/t84a/REV/ercrun/addrcmux3
Mon May 17 11:26:10 MDT 1999---wfjones@rndcs20---/rlu2/proj/t84a/REV/ercrun/addrcmux3
Mon May 17 13:16:59 MDT 1999---wfjones@rndcs2---/rlu2/proj/t84a/REV/ercrun/s_AF_Control
Mon May 17 13:55:59 MDT 1999---wfjones@rndcs17---/rlu2/proj/t84a/REV/ercrun/s_FIDOPT
Mon May 17 15:11:00 MDT 1999---wfjones@rndcs12---/rlu2/proj/t84a/REV/ercrun/s_FIDOPT
Mon May 17 15:35:59 MDT 1999---wfjones@rndcs6---/rlu2/proj/t84a/REV/ercrun/center_periph
Mon May 17 15:57:49 MDT 1999---wfjones@rndcs19---/rlu2/proj/t84a/REV/ercrun/s_clock
Mon May 17 17:09:11 MDT 1999---wfjones@rndcs6---/rlu2/proj/t84a/REV/ercrun/s_command
Mon May 17 17:14:09 MDT 1999---wfjones@rndcs6---/rlu2/proj/t84a/REV/ercrun/s_raschain
Tue May 18 10:03:29 MDT 1999---wfjones@rndcs9---/rlu2/proj/t84a/REV/ercrun/s_control
Tue May 18 10:12:05 MDT 1999---wfjones@rndcs19---/rlu2/proj/t84a/REV/ercrun/s_control_2
Tue May 18 10:15:17 MDT 1999---wfjones@rndcs19---/rlu2/proj/t84a/REV/ercrun/s_FIDOPT
Tue May 18 10:26:05 MDT 1999---wfjones@rndcs2---/rlu2/proj/t84a/REV/ercrun/s_address
Tue May 18 10:30:56 MDT 1999---wfjones@rndcs20---/rlu2/proj/t84a/REV/ercrun/s_dq
Tue May 18 13:00:53 MDT 1999---wfjones@rndcs19---/rlu2/proj/t84a/REV/ercrun/s_dq
Tue May 18 14:50:33 MDT 1999---wfjones@rndcs8---/rlu2/proj/t84a/REV/ercrun/s_bstcnter
Tue May 18 15:02:52 MDT 1999---ramecier@rndcs4---/rlu17/proj/y95a/REV/ercrun/vccp_dist_clamp
Tue May 18 15:09:17 MDT 1999---ramecier@rndcs8---/rlu17/proj/y95a/REV/ercrun/vccp_dist_clamp
Tue May 18 15:10:24 MDT 1999---ramecier@rndcs13---/rlu17/proj/y95a/REV/ercrun/vccp_dist_clamp
Tue May 18 15:31:09 MDT 1999---wfjones@rndcs14---/rlu2/proj/t84a/REV/ercrun/s_testmodes
Tue May 18 16:35:29 MDT 1999---wfjones@rndcs17---/rlu2/proj/t84a/REV/ercrun/dll_top
Tue May 18 16:34:23 MDT 1999---wfjones@rndcs4---/rlu2/proj/t84a/REV/ercrun/dll_top
Tue May 18 16:39:26 MDT 1999---wfjones@rndcs14---/rlu2/proj/t84a/REV/ercrun/pads_left
Wed May 19 09:19:05 MDT 1999---wfjones@rndcs7---/rlu2/proj/t84a/REV/ercrun/pads_right
Wed May 19 09:50:24 MDT 1999---wfjones@rndcs12---/rlu2/proj/t84a/REV/ercrun/vbbcntrl
Wed May 19 10:02:43 MDT 1999---wfjones@rndcs12---/rlu2/proj/t84a/REV/ercrun/vccpcntrl
Wed May 19 10:08:33 MDT 1999---wfjones@rndcs8---/rlu2/proj/t84a/REV/ercrun/center_right_spare
Wed May 19 10:15:03 MDT 1999---wfjones@rndcs14---/rlu2/proj/t84a/REV/ercrun/s_analog_left
Wed May 19 10:41:51 MDT 1999---wfjones@rndcs6---/rlu2/proj/t84a/REV/ercrun/center_left_spare
Wed May 19 10:52:14 MDT 1999---wfjones@rndcs11---/rlu2/proj/t84a/REV/ercrun/s_sref
Wed May 19 10:55:35 MDT 1999---wfjones@rndcs14---/rlu2/proj/t84a/REV/ercrun/16M_rowred
Wed May 19 11:00:45 MDT 1999---wfjones@rndcs19---/rlu2/proj/t84a/REV/ercrun/16M_RED_control2
Wed May 19 11:02:54 MDT 1999---wfjones@rndcs12---/rlu2/proj/t84a/REV/ercrun/16M_colred
Wed May 19 11:07:20 MDT 1999---wfjones@rndcs7---/rlu2/proj/t84a/REV/ercrun/dvc2_gen
Wed May 19 11:10:17 MDT 1999---wfjones@rndcs4---/rlu2/proj/t84a/REV/ercrun/topo
Wed May 19 11:16:36 MDT 1999---wfjones@rndcs14---/rlu2/proj/t84a/REV/ercrun/s_8megcntl
Wed May 19 11:24:00 MDT 1999---wfjones@rndcs7---/rlu2/proj/t84a/REV/ercrun/datadrv_ddr
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Wed May 19 11:24:37 MDT 1999---wfjones@rndcs14---/rlu2/proj/t84a/REV/ercrun/vccp_core_y64r
Wed May 19 11:41:13 MDT 1999---wfjones@rndcs5---/rlu2/proj/t84a/REV/ercrun/vbb_core_2phase
Wed May 19 11:41:54 MDT 1999---wfjones@rndcs15---/rlu2/proj/t84a/REV/ercrun/cde_spare
Wed May 19 11:45:53 MDT 1999---wfjones@rndcs8---/rlu2/proj/t84a/REV/ercrun/8meg_readwrite
Thu Jun 17 11:27:20 MDT 1999---bprotz@rndcs20---/rlu12/proj/d72g/REV/ercrun/bot
Thu Jun 17 22:06:26 MDT 1999---bprotz@rndcs12---/rlu12/proj/d72g/REV/ercrun/bot
Mon Jun 21 15:09:07 MDT 1999---kkalpakj@rndcs6---/rlu14/proj/s91a/REV/ercrun/ERC_TEST
Mon Jun 21 15:37:09 MDT 1999---kkalpakj@rndcs12---/rlu14/proj/s91a/REV/ercrun/ERC_TEST
Tue Jun 29 10:52:52 MDT 1999---bprotz@rndcs7---/rlu12/proj/d72g/REV/ercrun/d72gq
Fri Jul  9 14:49:15 MDT 1999---kkalpakj@rndcs11---/rlu14/proj/s91a/REV/ercrun/DIBUF_new
Fri Jul  9 15:11:41 MDT 1999---kkalpakj@rndcs5---/rlu14/proj/s91a/REV/ercrun/datain_delay
Fri Jul  9 15:31:36 MDT 1999---kkalpakj@rndcs4---/rlu14/proj/s91a/REV/ercrun/datain_delay
Fri Jul  9 15:41:33 MDT 1999---kkalpakj@rndcs7---/rlu14/proj/s91a/REV/ercrun/datain_delay
Mon Jul 12 15:20:47 MDT 1999---kkalpakj@rndcs2---/rlu14/proj/s91a/REV/ercrun/datain_delay
Mon Jul 12 15:22:57 MDT 1999---kkalpakj@rndcs16---/rlu14/proj/s91a/REV/ercrun/datain_delay
Mon Jul 12 15:23:49 MDT 1999---kkalpakj@rndcs16---/rlu14/proj/s91a/REV/ercrun/datain_delay
Tue Jul 13 16:37:00 MDT 1999---kkalpakj@rndcs15---/rlu14/proj/s91a/REV/ercrun/INBUFEN
Tue Jul 13 16:40:26 MDT 1999---kkalpakj@rndcs15---/rlu14/proj/s91a/REV/ercrun/INBUFEN
Tue Jul 13 16:41:33 MDT 1999---kkalpakj@rndcs15---/rlu14/proj/s91a/REV/ercrun/INBUFEN
Wed Jul 14 09:26:31 MDT 1999---kkalpakj@rndcs14---/rlu14/proj/s91a/REV/ercrun/ERC_TEST
Wed Jul 14 09:34:26 MDT 1999---kkalpakj@rndcs18---/rlu14/proj/s91a/REV/ercrun/kmk
Wed Jul 14 09:39:43 MDT 1999---kkalpakj@rndcs14---/rlu14/proj/s91a/REV/ercrun/kmk
Wed Jul 14 09:41:44 MDT 1999---kkalpakj@rndcs14---/rlu14/proj/s91a/REV/ercrun/ERC_TEST
Wed Jul 14 09:52:58 MDT 1999---kkalpakj@rndcs11---/rlu14/proj/s91a/REV/ercrun/ERC_TEST
Wed Jul 14 10:38:59 MDT 1999---bthompso@rndcs10---/rlu14/proj/s91a/REV/ercrun/CONTROL_A
Wed Jul 14 10:50:54 MDT 1999---bthompso@rndcs8---/rlu14/proj/s91a/REV/ercrun/CONTROL_A
Wed Jul 14 10:55:53 MDT 1999---bthompso@rndcs20---/rlu14/proj/s91a/REV/ercrun/CONTROL_A
Wed Jul 14 10:57:34 MDT 1999---bthompso@rndcs4---/rlu14/proj/s91a/REV/ercrun/CONTROL_A
Wed Jul 14 11:00:08 MDT 1999---bthompso@rndcs17---/rlu14/proj/s91a/REV/ercrun/CONTROL_A
Wed Jul 14 11:02:01 MDT 1999---bthompso@rndcs11---/rlu14/proj/s91a/REV/ercrun/CONTROL_A
Wed Jul 14 11:03:19 MDT 1999---bthompso@rndcs17---/rlu14/proj/s91a/REV/ercrun/CONTROL_A
Wed Jul 14 11:08:28 MDT 1999---bthompso@rndcs10---/rlu14/proj/s91a/REV/ercrun/inpad
Wed Jul 14 11:39:57 MDT 1999---bthompso@rndcs7---/rlu14/proj/s91a/REV/ercrun/inpad
Wed Jul 14 11:43:38 MDT 1999---bthompso@rndcs17---/rlu14/proj/s91a/REV/ercrun/CD2
Wed Jul 14 11:45:20 MDT 1999---bthompso@rndcs17---/rlu14/proj/s91a/REV/ercrun/CD2
Wed Jul 14 11:48:55 MDT 1999---bthompso@rndcs13---/rlu14/proj/s91a/REV/ercrun/CECNTL
Wed Jul 14 11:51:07 MDT 1999---bthompso@rndcs13---/rlu14/proj/s91a/REV/ercrun/WECNTL
Wed Jul 14 14:32:10 MDT 1999---bthompso@rndcs7---/rlu14/proj/s91a/REV/ercrun/WECNTL
Wed Jul 14 14:40:12 MDT 1999---bthompso@rndcs17---/rlu14/proj/s91a/REV/ercrun/WECNTL
Wed Jul 14 14:41:24 MDT 1999---bthompso@rndcs8---/rlu14/proj/s91a/REV/ercrun/WECNTL
Wed Jul 14 14:42:32 MDT 1999---bthompso@rndcs4---/rlu14/proj/s91a/REV/ercrun/CONTROL_A
Wed Jul 14 14:47:41 MDT 1999---bthompso@rndcs11---/rlu14/proj/s91a/REV/ercrun/OEBUFFER
Wed Jul 14 14:49:26 MDT 1999---bthompso@rndcs16---/rlu14/proj/s91a/REV/ercrun/CLKCONTROL
Wed Jul 14 14:55:28 MDT 1999---bthompso@rndcs4---/rlu14/proj/s91a/REV/ercrun/CLKCONTROL
Wed Jul 14 14:59:18 MDT 1999---bthompso@rndcs11---/rlu14/proj/s91a/REV/ercrun/CLKCONTROL
Wed Jul 14 15:02:32 MDT 1999---bthompso@rndcs11---/rlu14/proj/s91a/REV/ercrun/CLKCONTROL
Wed Jul 14 15:03:45 MDT 1999---bthompso@rndcs20---/rlu14/proj/s91a/REV/ercrun/CONTROL_A
Wed Jul 14 15:04:34 MDT 1999---bthompso@rndcs14---/rlu14/proj/s91a/REV/ercrun/CONTROL_B
Wed Jul 14 15:16:49 MDT 1999---bthompso@rndcs8---/rlu14/proj/s91a/REV/ercrun/CONTROL_B
Wed Jul 14 15:18:08 MDT 1999---bthompso@rndcs4---/rlu14/proj/s91a/REV/ercrun/CONTROL_B
Wed Jul 14 15:22:28 MDT 1999---bthompso@rndcs16---/rlu14/proj/s91a/REV/ercrun/CONTROL_B
Wed Jul 14 15:24:04 MDT 1999---bthompso@rndcs20---/rlu14/proj/s91a/REV/ercrun/RSPD_BLOCK
Wed Jul 14 15:29:18 MDT 1999---bthompso@rndcs4---/rlu14/proj/s91a/REV/ercrun/RSPD_BLOCK
Wed Jul 14 15:33:04 MDT 1999---bthompso@rndcs2---/rlu14/proj/s91a/REV/ercrun/HALFSEL
Wed Jul 14 15:35:34 MDT 1999---bthompso@rndcs13---/rlu14/proj/s91a/REV/ercrun/DATACLK
Wed Jul 14 15:36:10 MDT 1999---bthompso@rndcs16---/rlu14/proj/s91a/REV/ercrun/DATACLK
Wed Jul 14 15:45:11 MDT 1999---bthompso@rndcs13---/rlu14/proj/s91a/REV/ercrun/TOP
Wed Jul 14 16:55:36 MDT 1999---bthompso@rndcs17---/rlu14/proj/s91a/REV/ercrun/TOP
Thu Jul 15 16:36:53 MDT 1999---bthompso@rndcs8---/rlu14/proj/s91a/REV/ercrun/TOP
Fri Jul 16 10:27:08 MDT 1999---bthompso@rndcs17---/rlu14/proj/s91a/REV/ercrun/TOP
Fri Jul 16 10:47:27 MDT 1999---bthompso@rndcs15---/rlu14/proj/s91a/REV/ercrun/TOP
Fri Jul 16 12:33:47 MDT 1999---bthompso@rndcs8---/rlu14/proj/s91a/REV/ercrun/BOTTOM
Fri Jul 16 13:00:14 MDT 1999---bthompso@rndcs15---/rlu14/proj/s91a/REV/ercrun/BOTTOM
Fri Jul 16 13:06:03 MDT 1999---bthompso@rndcs8---/rlu14/proj/s91a/REV/ercrun/BOTTOM
Fri Jul 16 13:15:47 MDT 1999---bthompso@rndcs20---/rlu14/proj/s91a/REV/ercrun/TOP
Wed Jul 21 08:47:39 MDT 1999---kkalpakj@rndcs16---/rlu14/proj/s91a/REV/ercrun/NGATEREF2
Wed Jul 21 09:07:44 MDT 1999---kkalpakj@rndcs5---/rlu14/proj/s91a/REV/ercrun/BANDGAP3
Wed Jul 21 09:52:48 MDT 1999---kkalpakj@rndcs6---/rlu14/proj/s91a/REV/ercrun/ERC_TEST_small
Wed Jul 21 09:56:27 MDT 1999---kkalpakj@rndcs20---/rlu14/proj/s91a/REV/ercrun/ERC_TEST_small
Wed Jul 21 09:59:52 MDT 1999---kkalpakj@rndcs8---/rlu14/proj/s91a/REV/ercrun/ERC_TEST_small
Wed Jul 21 10:33:58 MDT 1999---bthompso@rndcs7---/rlu14/proj/s91a/REV/ercrun/TOP
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Wed Jul 21 10:44:25 MDT 1999---bthompso@rndcs9---/rlu14/proj/s91a/REV/ercrun/TOP
Wed Jul 21 10:51:40 MDT 1999---bthompso@rndcs10---/rlu14/proj/s91a/REV/ercrun/BOTTOM
Wed Jul 21 10:57:46 MDT 1999---bthompso@rndcs17---/rlu14/proj/s91a/REV/ercrun/BOTTOM
Wed Jul 21 12:53:04 MDT 1999---bthompso@rndcs8---/rlu14/proj/s91a/REV/ercrun/TOP
Thu Jul 22 15:30:10 MDT 1999---bthompso@rndcs11---/rlu14/proj/s91a/REV/ercrun/TOP
Thu Jul 22 15:40:31 MDT 1999---bthompso@rndcs16---/rlu14/proj/s91a/REV/ercrun/TOP
Thu Jul 22 15:56:45 MDT 1999---bthompso@rndcs9---/rlu14/proj/s91a/REV/ercrun/TOP
Thu Jul 22 16:30:28 MDT 1999---bthompso@rndcs9---/rlu14/proj/s91a/REV/ercrun/BOTTOM
Thu Jul 22 17:00:36 MDT 1999---bthompso@rndcs17---/rlu14/proj/s91a/REV/ercrun/BOTTOM
Tue Jul 27 10:56:43 MDT 1999---bthompso@rndcs16---/rlu14/proj/s91a/REV/ercrun/SIDE
Tue Jul 27 11:16:42 MDT 1999---bthompso@rndcs8---/rlu14/proj/s91a/REV/ercrun/SIDE
Tue Jul 27 11:28:21 MDT 1999---bthompso@rndcs18---/rlu14/proj/s91a/REV/ercrun/SIDE
Tue Jul 27 11:33:10 MDT 1999---bthompso@rndcs17---/rlu14/proj/s91a/REV/ercrun/SIDE
Tue Jul 27 11:36:45 MDT 1999---bthompso@rndcs8---/rlu14/proj/s91a/REV/ercrun/RIGHT
Tue Jul 27 12:09:51 MDT 1999---bthompso@rndcs18---/rlu14/proj/s91a/REV/ercrun/RIGHT
Tue Jul 27 12:38:00 MDT 1999---bthompso@rndcs14---/rlu14/proj/s91a/REV/ercrun/LEFT
Tue Jul 27 12:44:43 MDT 1999---bthompso@rndcs8---/rlu14/proj/s91a/REV/ercrun/LEFT
Tue Jul 27 13:56:48 MDT 1999---bthompso@rndcs4---/rlu14/proj/s91a/REV/ercrun/S91AF
Tue Jul 27 14:09:48 MDT 1999---bthompso@rndcs4---/rlu14/proj/s91a/REV/ercrun/ARRBLK2_25M
Tue Jul 27 14:19:36 MDT 1999---bthompso@rndcs8---/rlu14/proj/s91a/REV/ercrun/GWL_BLOCK
Tue Jul 27 14:20:23 MDT 1999---bthompso@rndcs20---/rlu14/proj/s91a/REV/ercrun/THROAT
Tue Jul 27 14:21:52 MDT 1999---bthompso@rndcs4---/rlu14/proj/s91a/REV/ercrun/ARRQUAD
Tue Jul 27 14:53:35 MDT 1999---bthompso@rndcs19---/rlu14/proj/s91a/REV/ercrun/ARRBLK4_5M_CENTER
Tue Jul 27 16:49:51 MDT 1999---bthompso@rndcs20---/rlu14/proj/s91a/REV/ercrun/SETUP_HOLD
Tue Jul 27 17:23:50 MDT 1999---bthompso@rndcs14---/rlu14/proj/s91a/REV/ercrun/SETUP_HOLD
Tue Jul 27 17:36:25 MDT 1999---bthompso@rndcs19---/rlu14/proj/s91a/REV/ercrun/SETUP_HOLD
Tue Aug  3 09:22:06 MDT 1999---gblodget@rndcs15---/rlu11/proj/d84b/REV/ercrun/CENTER_CHIP
Tue Aug  3 16:45:49 MDT 1999---gblodget@rndcs16---/rlu11/proj/d84b/REV/ercrun/CENTER_CHIP
Wed Aug 11 10:24:47 MDT 1999---gblodget@rndcs11---/rlu11/proj/d84b/REV/ercrun/CENTER_CHIP
Thu Aug 12 15:28:30 MDT 1999---bthompso@rndcs6---/rlu14/proj/s91a/REV/ercrun/SETUP_HOLD
Thu Aug 12 15:37:29 MDT 1999---bthompso@rndcs14---/rlu14/proj/s91a/REV/ercrun/SETUP_HOLD
Thu Aug 19 10:50:26 MDT 1999---tcowles@rndcs12---/rlu17/proj/y95a/REV/ercrun/data_out
Thu Aug 19 12:43:20 MDT 1999---tcowles@rndcs2---/rlu17/proj/y95a/REV/ercrun/data_out
Thu Aug 19 12:59:15 MDT 1999---tcowles@rndcs9---/rlu17/proj/y95a/REV/ercrun/regulator
Thu Aug 19 15:56:34 MDT 1999---tcowles@rndcs15---/rlu17/proj/y95a/REV/ercrun/rowaddr_track
Thu Aug 19 15:58:48 MDT 1999---tcowles@rndcs6---/rlu17/proj/y95a/REV/ercrun/datagap
Sat Aug 21 10:16:51 MDT 1999---tcowles@rndcs2---/rlu17/proj/y95a/REV/ercrun/vccp_pump
Mon Aug 23 16:34:30 MDT 1999---tcowles@rndcs5---/rlu17/proj/y95a/REV/ercrun/regulator
Mon Aug 23 16:44:49 MDT 1999---tcowles@rndcs4---/rlu17/proj/y95a/REV/ercrun/regulator
Mon Aug 23 16:56:18 MDT 1999---tcowles@rndcs17---/rlu17/proj/y95a/REV/ercrun/regulator
Wed Aug 25 09:25:06 MDT 1999---tcowles@rndcs11---/rlu17/proj/y95a/REV/ercrun/data_out
Wed Aug 25 10:41:48 MDT 1999---tcowles@rndcs4---/rlu17/proj/y95a/REV/ercrun/data_out
Wed Aug 25 10:42:57 MDT 1999---tcowles@rndcs4---/rlu17/proj/y95a/REV/ercrun/data_out
Wed Aug 25 10:59:37 MDT 1999---tcowles@rndcs19---/rlu17/proj/y95a/REV/ercrun/data_out
Wed Aug 25 13:45:59 MDT 1999---tcowles@rndcs14---/rlu17/proj/y95a/REV/ercrun/regulator
Wed Aug 25 14:16:51 MDT 1999---tcowles@rndcs15---/rlu17/proj/y95a/REV/ercrun/vccp_pump
Wed Aug 25 14:21:09 MDT 1999---tcowles@rndcs8---/rlu17/proj/y95a/REV/ercrun/vccp_pump
Wed Aug 25 15:14:36 MDT 1999---tcowles@rndcs13---/rlu17/proj/y95a/REV/ercrun/vbb_pump
Wed Aug 25 15:21:19 MDT 1999---tcowles@rndcs2---/rlu17/proj/y95a/REV/ercrun/datagap
Fri Sep  3 10:11:46 MDT 1999---tcowles@rndcs21---/rlu17/proj/y95a/REV/ercrun/y95aQ
Thu Sep 16 09:24:04 MDT 1999---rtaylor@rtaylor---/home/rtaylor/df2/skill4/si
Fri Sep 24 09:28:57 MDT 1999---bthompso@rndcs22---/rlu14/proj/s91a/REV/ercrun/TOP
Fri Sep 24 10:00:00 MDT 1999---bthompso@rndcs22---/rlu14/proj/s91a/REV/ercrun/TOP
Fri Sep 24 10:11:52 MDT 1999---bthompso@rndcs21---/rlu14/proj/s91a/REV/ercrun/TOP
Fri Sep 24 10:29:06 MDT 1999---bthompso@rndcs11---/rlu14/proj/s91a/REV/ercrun/BOTTOM
Fri Sep 24 10:35:32 MDT 1999---bthompso@rndcs13---/rlu14/proj/s91a/REV/ercrun/SIDE
Fri Sep 24 10:48:20 MDT 1999---bthompso@rndcs22---/rlu14/proj/s91a/REV/ercrun/SIDE
Fri Sep 24 10:53:28 MDT 1999---bthompso@rndcs21---/rlu14/proj/s91a/REV/ercrun/RIGHT
Fri Sep 24 11:01:12 MDT 1999---bthompso@rndcs20---/rlu14/proj/s91a/REV/ercrun/RIGHT
Fri Sep 24 11:07:54 MDT 1999---bthompso@rndcs19---/rlu14/proj/s91a/REV/ercrun/RIGHT
Fri Sep 24 11:10:25 MDT 1999---bthompso@rndcs21---/rlu14/proj/s91a/REV/ercrun/LEFT
Fri Sep 24 11:18:22 MDT 1999---bthompso@rndcs19---/rlu14/proj/s91a/REV/ercrun/LEFT
Fri Sep 24 11:23:02 MDT 1999---bthompso@rndcs19---/rlu14/proj/s91a/REV/ercrun/RIGHT
Fri Sep 24 12:06:14 MDT 1999---bthompso@rndcs19---/rlu14/proj/s91a/REV/ercrun/S91AF
Fri Sep 24 12:57:13 MDT 1999---bthompso@rndcs20---/rlu14/proj/s91a/REV/ercrun/TOP
Fri Sep 24 13:23:28 MDT 1999---bthompso@rndcs23---/rlu14/proj/s91a/REV/ercrun/ARRBLK4_5M
Fri Sep 24 13:38:21 MDT 1999---bthompso@rndcs23---/rlu14/proj/s91a/REV/ercrun/BOTTOM
Fri Sep 24 14:16:47 MDT 1999---bthompso@rndcs21---/rlu14/proj/s91a/REV/ercrun/ARRQUAD
Fri Sep 24 15:36:26 MDT 1999---bthompso@rndcs21---/rlu14/proj/s91a/REV/ercrun/ARRBLK2_25M
Fri Sep 24 16:33:15 MDT 1999---bthompso@rndcs23---/rlu14/proj/s91a/REV/ercrun/ARRBLK2_25M
Fri Sep 24 16:37:04 MDT 1999---bthompso@rndcs19---/rlu14/proj/s91a/REV/ercrun/S91AF
```

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Fri Sep 24 17:06:13 MDT 1999---bthompso@rndcs20---/rlu14/proj/s91a/REV/ercrun/S91AF
Wed Sep 29 09:57:34 MDT 1999---bthompso@rndcs20---/rlu14/proj/s91a/REV/ercrun/S91AF
Wed Sep 29 12:45:44 MDT 1999---bthompso@rndcs21---/rlu14/proj/s91a/REV/ercrun/S91AF
Thu Sep 30 10:27:43 MDT 1999---bthompso@rndcs20---/rlu14/proj/s91a/REV/ercrun/S91AF
Mon Nov  1 10:15:04 MST 1999---kkalpakj@rndcs10---/rlu14/proj/s91a/REV/ercrun/S91AF
Mon Nov  1 12:43:40 MST 1999---kkalpakj@rndcs14---/rlu14/proj/s91a/REV/ercrun/S91AF
Wed Nov 24 09:54:35 MST 1999---kkalpakj@rndcs17---/rlu14/proj/s91a/REV/ercrun/S91AF
Wed Nov 24 12:05:02 MST 1999---kkalpakj@rndcs16---/rlu14/proj/s91a/REV/ercrun/S91AF
Mon Nov 29 10:36:22 MST 1999---kkalpakj@rndcs15---/rlu14/proj/s91a/REV/ercrun/S91AF
Wed Dec  1 14:08:22 MST 1999---lweber@rndcs22---/rlu15/proj/s78a/REV/ercrun/qcell
Mon Dec  6 11:23:49 MST 1999---bkeeth@rndcs4---/rlu17/proj/e85a/REV/ercrun/padTier_D
Mon Dec  6 12:22:14 MST 1999---bkeeth@rndcs8---/rlu17/proj/e85a/REV/ercrun/padTier_D
Mon Dec  6 12:25:59 MST 1999---bkeeth@rndcs16---/rlu17/proj/e85a/REV/ercrun/padTier_D
Mon Dec  6 13:59:03 MST 1999---bkeeth@rndcs4---/rlu17/proj/e85a/REV/ercrun/padTier_D
Mon Dec  6 14:13:54 MST 1999---bkeeth@rndcs12---/rlu17/proj/e85a/REV/ercrun/padTier_D
Mon Dec  6 15:06:13 MST 1999---bkeeth@rndcs17---/rlu17/proj/e85a/REV/ercrun/padTier_D
Mon Dec  6 15:10:06 MST 1999---bkeeth@rndcs21---/rlu17/proj/e85a/REV/ercrun/padTier_D
Mon Dec  6 15:13:36 MST 1999---bkeeth@rndcs7---/rlu17/proj/e85a/REV/ercrun/padTier_D
Mon Dec  6 15:23:07 MST 1999---jcullum@rndcs2---/rlu28/proj/y86a/REV/ercrun/y86aQ
Mon Dec  6 15:23:25 MST 1999---bkeeth@rndcs7---/rlu17/proj/e85a/REV/ercrun/padTier_D
Mon Dec  6 15:53:23 MST 1999---bkeeth@rndcs15---/rlu17/proj/e85a/REV/ercrun/padTier_D
Tue Dec  7 10:13:57 MST 1999---bkeeth@rndcs16---/rlu17/proj/e85a/REV/ercrun/padTier_D
Tue Dec  7 12:10:29 MST 1999---bkeeth@rndcs20---/rlu17/proj/e85a/REV/ercrun/padTier_D
Tue Dec  7 13:01:58 MST 1999---bkeeth@rndcs21---/rlu17/proj/e85a/REV/ercrun/padTier_D
Tue Dec  7 13:22:33 MST 1999---bkeeth@rndcs12---/rlu17/proj/e85a/REV/ercrun/padTier_D
Tue Dec  7 14:12:55 MST 1999---bkeeth@rndcs20---/rlu17/proj/e85a/REV/ercrun/padTier_D
Tue Dec  7 14:26:50 MST 1999---bkeeth@rndcs18---/rlu17/proj/e85a/REV/ercrun/padTier_D
Tue Dec  7 14:36:31 MST 1999---bkeeth@rndcs12---/rlu17/proj/e85a/REV/ercrun/padTier_D
Tue Dec  7 14:44:52 MST 1999---bkeeth@rndcs22---/rlu17/proj/e85a/REV/ercrun/padTier_D
Tue Dec  7 14:56:12 MST 1999---jcullum@rndcs20---/rlu28/proj/y86a/REV/ercrun/pwramp
Tue Dec  7 14:59:56 MST 1999---jcullum@rndcs20---/rlu28/proj/y86a/REV/ercrun/pwramp
Tue Dec  7 16:08:57 MST 1999---jcullum@rndcs21---/rlu28/proj/y86a/REV/ercrun/pwramp
Tue Dec  7 16:15:58 MST 1999---jcullum@rndcs21---/rlu28/proj/y86a/REV/ercrun/pwramp
Tue Dec  7 16:20:16 MST 1999---jcullum@rndcs12---/rlu28/proj/y86a/REV/ercrun/pwramp
Tue Dec  7 16:20:58 MST 1999---jcullum@rndcs12---/rlu28/proj/y86a/REV/ercrun/pwramp
Wed Dec  8 12:28:14 MST 1999---jcullum@rndcs5---/rlu28/proj/y86a/REV/ercrun/y86aQ
Thu Dec  9 15:48:21 MST 1999---jcullum@rndcs7---/rlu28/proj/y86a/REV/ercrun/y86aQ
Thu Jan  6 15:32:52 MST 2000---bprotz@rndcs21---/rlu22/proj/y95c/REV/ercrun/y95cQ
Tue Jan 11 06:56:13 MST 2000---bprotz@rndcs17---/rlu22/proj/y95c/REV/ercrun/y95cQ
Wed Jan 12 14:18:46 MST 2000---bprotz@rndcs6---/rlu22/proj/y95c/REV/ercrun/y95cQ
Tue Jan 18 09:48:20 MST 2000---cmorzano@rndcs13---/rlu21/proj/t96a/REV/ercrun/Fullchip
Thu Jan 20 08:55:26 MST 2000---bthompso@rndcs24---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Thu Jan 20 09:04:29 MST 2000---bthompso@rndcs15---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Thu Jan 20 10:35:38 MST 2000---bthompso@rndcs17---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Thu Jan 20 11:44:41 MST 2000---bthompso@rndcs24---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Thu Jan 20 17:11:27 MST 2000---bthompso@rndcs15---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Fri Jan 21 09:30:52 MST 2000---bthompso@rndcs17---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Fri Jan 21 11:32:47 MST 2000---bthompso@rndcs23---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Fri Jan 21 12:01:08 MST 2000---bthompso@rndcs19---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Fri Jan 21 12:06:39 MST 2000---bthompso@rndcs12---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Fri Jan 21 12:12:58 MST 2000---bthompso@rndcs10---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Fri Jan 21 13:49:46 MST 2000---bthompso@rndcs23---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Fri Jan 21 13:57:38 MST 2000---bthompso@rndcs21---/rlu27/proj/s93a/REV/ercrun/TOP
Fri Jan 21 14:28:08 MST 2000---bthompso@rndcs16---/rlu27/proj/s93a/REV/ercrun/TOP
Thu Jan 27 12:30:57 MST 2000---bthompso@rndcs19---/rlu27/proj/s93a/REV/ercrun/SIDE
Thu Jan 27 13:26:29 MST 2000---bthompso@rndcs18---/rlu27/proj/s93a/REV/ercrun/SIDE
Thu Jan 27 15:25:20 MST 2000---bthompso@rndcs9---/rlu27/proj/s93a/REV/ercrun/SIDE
Thu Jan 27 17:29:26 MST 2000---bthompso@rndcs18---/rlu27/proj/s93a/REV/ercrun/SIDE
Fri Jan 28 11:12:25 MST 2000---bthompso@rndcs17---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Fri Jan 28 11:35:22 MST 2000---bthompso@rndcs20---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Fri Jan 28 11:40:52 MST 2000---bthompso@rndcs7---/rlu27/proj/s93a/REV/ercrun/TOP
Fri Jan 28 12:12:58 MST 2000---bthompso@rndcs15---/rlu27/proj/s93a/REV/ercrun/TOP
Fri Jan 28 12:19:00 MST 2000---bthompso@rndcs18---/rlu27/proj/s93a/REV/ercrun/SIDE
Fri Jan 28 12:57:33 MST 2000---bthompso@rndcs7---/rlu27/proj/s93a/REV/ercrun/SIDE
Mon Jan 31 10:49:00 MST 2000---jsher@rndcs12---/rlu27/proj/s93a/REV/ercrun/CENTER_left
Mon Jan 31 11:57:37 MST 2000---bthompso@rndcs5---/rlu27/proj/s93a/REV/ercrun/LEFT
Mon Jan 31 12:38:30 MST 2000---bthompso@rndcs17---/rlu27/proj/s93a/REV/ercrun/LEFT
Mon Jan 31 12:50:21 MST 2000---bthompso@rndcs14---/rlu27/proj/s93a/REV/ercrun/LEFT
Mon Jan 31 14:00:25 MST 2000---bthompso@rndcs18---/rlu27/proj/s93a/REV/ercrun/LEFT
Mon Jan 31 14:07:44 MST 2000---bthompso@rndcs12---/rlu27/proj/s93a/REV/ercrun/RIGHT
Mon Jan 31 15:10:07 MST 2000---bthompso@rndcs24---/rlu27/proj/s93a/REV/ercrun/RIGHT
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Mon Jan 31 15:40:42 MST 2000---bthompso@rndcs17---/r1u27/proj/s93a/REV/ercrun/RIGHT
Mon Jan 31 17:36:50 MST 2000---jsher@rndcs24---/r1u27/proj/s93a/REV/ercrun/CENTER_left
Tue Feb  1 09:22:23 MST 2000---jsher@rndcs11---/r1u27/proj/s93a/REV/ercrun/CENTER_left
Tue Feb  1 13:28:26 MST 2000---jsher@rndcs24---/r1u27/proj/s93a/REV/ercrun/CENTER_left
Tue Feb  1 14:03:54 MST 2000---jsher@rndcs9---/r1u27/proj/s93a/REV/ercrun/CENTER_left
Tue Feb  1 15:01:53 MST 2000---jsher@rndcs10---/r1u27/proj/s93a/REV/ercrun/CENTER_left
Tue Feb  1 15:34:09 MST 2000---bthompso@rndcs13---/r1u27/proj/s93a/REV/ercrun/inpad
Tue Feb  1 15:38:42 MST 2000---bthompso@rndcs21---/r1u27/proj/s93a/REV/ercrun/inpad
Tue Feb  1 15:42:12 MST 2000---bthompso@rndcs7---/r1u27/proj/s93a/REV/ercrun/inpad
Tue Feb  1 15:43:37 MST 2000---bthompso@rndcs11---/r1u27/proj/s93a/REV/ercrun/inpad
Tue Feb  1 15:45:11 MST 2000---bthompso@rndcs10---/r1u27/proj/s93a/REV/ercrun/inpad
Tue Feb  1 15:46:26 MST 2000---bthompso@rndcs14---/r1u27/proj/s93a/REV/ercrun/inpad
Tue Feb  1 15:48:55 MST 2000---bthompso@rndcs6---/r1u27/proj/s93a/REV/ercrun/inpad
Tue Feb  1 15:52:04 MST 2000---bthompso@rndcs21---/r1u27/proj/s93a/REV/ercrun/inpad
Tue Feb  1 15:58:15 MST 2000---jsher@rndcs11---/r1u27/proj/s93a/REV/ercrun/CENTER_left
Tue Feb  1 16:06:24 MST 2000---jsher@rndcs13---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Tue Feb  1 16:16:25 MST 2000---jsher@rndcs21---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Tue Feb  1 16:26:22 MST 2000---jsher@rndcs9---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Tue Feb  1 16:28:15 MST 2000---jsher@rndcs7---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 08:30:57 MST 2000---jsher@rndcs10---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 08:32:11 MST 2000---jsher@rndcs24---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 08:39:02 MST 2000---jsher@rndcs7---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 09:00:49 MST 2000---jsher@rndcs20---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 10:40:16 MST 2000---bthompso@rndcs17---/r1u27/proj/s93a/REV/ercrun/BOTTOM
Wed Feb  2 10:47:09 MST 2000---bthompso@rndcs13---/r1u27/proj/s93a/REV/ercrun/BOTTOM
Wed Feb  2 10:50:07 MST 2000---bthompso@rndcs19---/r1u27/proj/s93a/REV/ercrun/TOP
Wed Feb  2 10:52:27 MST 2000---bthompso@rndcs14---/r1u27/proj/s93a/REV/ercrun/TOP
Wed Feb  2 11:54:34 MST 2000---bthompso@rndcs9---/r1u27/proj/s93a/REV/ercrun/BOTTOM
Wed Feb  2 12:19:37 MST 2000---bthompso@rndcs6---/r1u27/proj/s93a/REV/ercrun/BOTTOM
Wed Feb  2 12:27:40 MST 2000---bthompso@rndcs7---/r1u27/proj/s93a/REV/ercrun/RIGHT
Wed Feb  2 12:34:38 MST 2000---bthompso@rndcs19---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 13:29:34 MST 2000---jsher@rndcs7---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 14:15:30 MST 2000---jsher@rndcs17---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 14:18:46 MST 2000---bthompso@rndcs14---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 14:19:04 MST 2000---jsher@rndcs14---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 15:32:49 MST 2000---bthompso@rndcs22---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 15:46:30 MST 2000---bthompso@rndcs22---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 15:58:06 MST 2000---bthompso@rndcs18---/r1u27/proj/s93a/REV/ercrun/ERC_testcase
Wed Feb  2 16:18:22 MST 2000---bthompso@rndcs7---/r1u27/proj/s93a/REV/ercrun/REGBLOCKCNTR
Wed Feb  2 16:20:08 MST 2000---bthompso@rndcs12---/r1u27/proj/s93a/REV/ercrun/REGBLOCKCNTR
Wed Feb  2 16:21:49 MST 2000---bthompso@rndcs14---/r1u27/proj/s93a/REV/ercrun/REGBLOCKCNTR
Wed Feb  2 16:23:46 MST 2000---bthompso@rndcs22---/r1u27/proj/s93a/REV/ercrun/REGBLOCKCNTR
Wed Feb  2 16:25:05 MST 2000---bthompso@rndcs22---/r1u27/proj/s93a/REV/ercrun/REGBLOCKCNTR
Wed Feb  2 16:52:09 MST 2000---bthompso@rndcs22---/r1u27/proj/s93a/REV/ercrun/REGBLOCKCNTR
Thu Feb  3 08:16:34 MST 2000---jsher@rndcs18---/r1u27/proj/s93a/REV/ercrun/CENTER_left
Thu Feb  3 15:07:28 MST 2000---jsher@rndcs23---/r1u27/proj/s93a/REV/ercrun/CENTER_left
Thu Feb  3 15:21:46 MST 2000---bthompso@rndcs8---/r1u27/proj/s93a/REV/ercrun/BOTTOM
Thu Feb  3 15:25:15 MST 2000---bthompso@rndcs8---/r1u27/proj/s93a/REV/ercrun/LEFT
Thu Feb  3 15:33:14 MST 2000---bthompso@rndcs20---/r1u27/proj/s93a/REV/ercrun/RIGHT
Thu Feb  3 15:40:41 MST 2000---bthompso@rndcs18---/r1u27/proj/s93a/REV/ercrun/TOP
Fri Feb  4 13:55:28 MST 2000---jsher@rndcs10---/r1u27/proj/s93a/REV/ercrun/CENTER_left
Fri Feb  4 16:23:52 MST 2000---bthompso@rndcs10---/r1u27/proj/s93a/REV/ercrun/CENTER_right
Fri Feb  4 16:50:00 MST 2000---bthompso@rndcs5---/r1u27/proj/s93a/REV/ercrun/CENTER_right
Mon Feb  7 08:11:12 MST 2000---jsher@rndcs12---/r1u27/proj/s93a/REV/ercrun/CENTER_left
Mon Feb  7 09:01:31 MST 2000---jsher@rndcs20---/r1u27/proj/s93a/REV/ercrun/SIDE
Mon Feb  7 09:06:58 MST 2000---jsher@rndcs17---/r1u27/proj/s93a/REV/ercrun/SIDE
Mon Feb  7 14:19:41 MST 2000---bthompso@rndcs4---/r1u27/proj/s93a/REV/ercrun/CENTER_right
Mon Feb  7 15:30:31 MST 2000---bthompso@rndcs9---/r1u27/proj/s93a/REV/ercrun/CENTER_right
Tue Feb  8 11:01:56 MST 2000---lkeller@rndcs22---/r1u15/proj/y15a/REVB/ercrun/y15aQ
Wed Feb  9 16:15:50 MST 2000---bthompso@rndcs9---/r1u27/proj/s93a/REV/ercrun/CENTER_right
Wed Feb  9 16:22:43 MST 2000---bthompso@rndcs9---/r1u27/proj/s93a/REV/ercrun/CENTER_right
Wed Feb  9 16:35:57 MST 2000---bthompso@rndcs11---/r1u27/proj/s93a/REV/ercrun/CENTER_new
Wed Feb  9 17:34:40 MST 2000---bthompso@rndcs19---/r1u27/proj/s93a/REV/ercrun/CENTER_new
Thu Feb 10 09:21:08 MST 2000---bthompso@rndcs16---/r1u27/proj/s93a/REV/ercrun/CENTER_new
Thu Feb 10 09:46:30 MST 2000---bthompso@rndcs8---/r1u27/proj/s93a/REV/ercrun/CENTER_new
Thu Feb 10 10:00:34 MST 2000---bthompso@rndcs8---/r1u27/proj/s93a/REV/ercrun/S93AF
Thu Feb 10 14:26:58 MST 2000---bthompso@rndcs20---/r1u27/proj/s93a/REV/ercrun/CENTER_new
Fri Feb 11 14:28:05 MST 2000---bthompso@rndcs2---/r1u27/proj/s93a/REV/ercrun/S93AF
Fri Feb 11 15:50:48 MST 2000---bthompso@rndcs7---/r1u27/proj/s93a/REV/ercrun/S93AF
Mon Feb 14 09:26:59 MST 2000---bthompso@rndcs16---/r1u27/proj/s93a/REV/ercrun/S93AF
Mon Feb 14 11:17:48 MST 2000---bthompso@rndcs8---/r1u27/proj/s93a/REV/ercrun/S93AF
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Mon Feb 14 12:25:37 MST 2000---bthompso@rndcs18---/rlu27/proj/s93a/REV/ercrun/S93AF
Fri Feb 18 08:19:16 MST 2000---bthompso@rndcs23---/rlu27/proj/s93a/REV/ercrun/CENTER_right
Fri Feb 18 09:26:50 MST 2000---bthompso@rndcs18---/rlu27/proj/s93a/REV/ercrun/LEFT
Fri Feb 18 09:40:53 MST 2000---bthompso@rndcs20---/rlu27/proj/s93a/REV/ercrun/RIGHT
Fri Feb 18 10:00:07 MST 2000---bthompso@rndcs4---/rlu27/proj/s93a/REV/ercrun/TOP
Fri Feb 18 10:15:33 MST 2000---bthompso@rndcs23---/rlu27/proj/s93a/REV/ercrun/TOP
Fri Feb 18 10:22:18 MST 2000---bthompso@rndcs10---/rlu27/proj/s93a/REV/ercrun/BOTTOM
Fri Feb 18 10:35:44 MST 2000---tmanning@rndcs21---/rlu9/proj/r85a/REV/ercrun/r85aQ
Fri Feb 18 10:42:21 MST 2000---bthompso@rndcs6---/rlu27/proj/s93a/REV/ercrun/CENTER_new
Wed Feb 23 14:24:34 MST 2000---tmanning@rndcs24---/rlu9/proj/r85a/REV/ercrun/r85aQ
Wed Feb 23 19:37:16 MST 2000---bprotz@rndcs24---/rlu22/proj/y95c/REV/ercrun/y95cQ
Mon Feb 28 14:05:38 MST 2000---huyvo@rndcs21---/rlu2/proj/x84b/DRAM/ercrun/vccp_better_pump
Mon Feb 28 14:09:09 MST 2000---huyvo@rndcs2---/rlu2/proj/x84b/DRAM/ercrun/vccp_better_pump

Fri Mar  3 11:07:59 MST 2000---rmurli@rndcs13---/rlu31/proj/y15b/REV/ercrun/vccp_pump
Fri Mar  3 11:31:27 MST 2000---rmurli@rndcs11---/rlu31/proj/y15b/REV/ercrun/vccp_pump
Fri Mar  3 11:46:09 MST 2000---rmurli@rndcs6---/rlu31/proj/y15b/REV/ercrun/vccp_pump
Fri Mar  3 11:49:21 MST 2000---rmurli@rndcs7---/rlu31/proj/y15b/REV/ercrun/regulator
Fri Mar  3 13:41:07 MST 2000---rmurli@rndcs18---/rlu31/proj/y15b/REV/ercrun/regulator
Fri Mar  3 14:09:43 MST 2000---rmurli@rndcs18---/rlu31/proj/y15b/REV/ercrun/regulator
Fri Mar  3 15:05:14 MST 2000---rmurli@rndcs20---/rlu31/proj/y15b/REV/ercrun/isodrv
Fri Mar  3 15:11:54 MST 2000---rmurli@rndcs18---/rlu31/proj/y15b/REV/ercrun/isodrv
Fri Mar  3 15:13:15 MST 2000---rmurli@rndcs18---/rlu31/proj/y15b/REV/ercrun/dqboot
Fri Mar  3 15:15:39 MST 2000---rmurli@rndcs20---/rlu31/proj/y15b/REV/ercrun/dqdrv
Fri Mar  3 15:18:46 MST 2000---rmurli@rndcs14---/rlu31/proj/y15b/REV/ercrun/dqdrv
Fri Mar  3 15:22:24 MST 2000---rmurli@rndcs6---/rlu31/proj/y15b/REV/ercrun/256rdecMidTRD
Fri Mar  3 15:31:25 MST 2000---rmurli@rndcs17---/rlu31/proj/y15b/REV/ercrun/regulator
Fri Mar  3 15:52:12 MST 2000---rmurli@rndcs17---/rlu31/proj/y15b/REV/ercrun/cgndDrv
Fri Mar  3 15:55:49 MST 2000---rmurli@rndcs18---/rlu31/proj/y15b/REV/ercrun/cgndDrv
Fri Mar  3 16:20:52 MST 2000---rmurli@rndcs2---/rlu31/proj/y15b/REV/ercrun/vccp_pump
Fri Mar  3 16:57:10 MST 2000---rmurli@rndcs15---/rlu31/proj/y15b/REV/ercrun/y15bQ
Sat Mar  4 20:53:22 MST 2000---rmurli@rndcs12---/rlu31/proj/y15b/REV/ercrun/y15bQ
Sat Mar  4 22:47:45 MST 2000---rmurli@rndcs10---/rlu31/proj/y15b/REV/ercrun/y15bQ
Mon Mar  6 10:39:03 MST 2000---rmurli@rndcs10---/rlu31/proj/y15b/REV/ercrun/y15bQ
Tue Mar  7 14:06:52 MST 2000---rmurli@rndcs19---/rlu31/proj/y15b/REV/ercrun/data_outt
Thu Mar  9 12:23:43 MST 2000---bkeeth@rndcs13---/rlu17/proj/e85a/REV/ercrun/Periph
Thu Mar  9 16:56:21 MST 2000---bkeeth@rndcs13---/rlu17/proj/e85a/REV/ercrun/Periph
Fri Mar 10 11:40:12 MST 2000---bkeeth@rndcs13---/rlu17/proj/e85a/REV/ercrun/144MGarray
Fri Mar 10 16:04:24 MST 2000---bkeeth@rndcs20---/rlu17/proj/e85a/REV/ercrun/144MGarray
Mon Mar 13 09:13:23 MST 2000---bkeeth@rndcs20---/rlu17/proj/e85a/REV/ercrun/Periph
Mon Mar 13 10:35:19 MST 2000---bkeeth@rndcs11---/rlu17/proj/e85a/REV/ercrun/Periph
Mon Mar 13 11:54:39 MST 2000---bkeeth@rndcs7---/rlu17/proj/e85a/REV/ercrun/144MGarray
Tue Mar 14 12:24:28 MST 2000---bthompso@rndcs24---/rlu27/proj/s93a/REV/ercrun/S93AF
Tue Mar 14 13:16:36 MST 2000---bthompso@rndcs2---/rlu27/proj/s93a/REV/ercrun/S93AF
Tue Mar 14 17:35:35 MST 2000---bthompso@rndcs14---/rlu27/proj/s93a/REV/ercrun/S93AF
Thu Mar 16 11:23:00 MST 2000---bthompso@rndcs18---/rlu27/proj/s93a/REV/ercrun/S93AF
Thu Mar 16 12:12:32 MST 2000---bthompso@rndcs18---/rlu27/proj/s93a/REV/ercrun/S93AF
Tue Mar 21 09:13:12 MST 2000---bthompso@rndcs8---/rlu27/proj/s93a/REV/ercrun/S93AF
Tue Mar 21 11:15:38 MST 2000---bthompso@rndcs9---/rlu27/proj/s93a/REV/ercrun/S93AF
Tue Mar 21 12:50:07 MST 2000---bthompso@rndcs4---/rlu27/proj/s93a/REV/ercrun/S93AF
Wed Mar 22 14:05:59 MST 2000---bprotz@rndcs23---/rlu22/proj/y95c/REV/ercrun/y95cQ
Wed Mar 22 15:02:36 MST 2000---bprotz@rndcs24---/rlu22/proj/y95c/REV/ercrun/y95cQ
Fri Mar 24 13:42:52 MST 2000---ghush@rndcs7---/rlu21/proj/t96a/REV/ercrun/Fullchip
Mon Mar 27 13:11:15 MST 2000---ghush@rndcs23---/rlu21/proj/t96a/REV/ercrun/Fullchip
Tue Mar 28 15:01:20 MST 2000---ghush@rndcs9---/rlu21/proj/t96a/REV/ercrun/Fullchip
Tue Mar 28 17:00:56 MST 2000---ghush@rndcs20---/rlu21/proj/t96a/REV/ercrun/Fullchip
Thu Mar 30 11:39:37 MST 2000---ghush@rndcs17---/rlu21/proj/t96a/REV/ercrun/Fullchip
Fri Mar 31 16:12:44 MST 2000---ghush@rndcs10---/rlu21/proj/t96a/REV/ercrun/Fullchip
Mon Apr  3 10:26:10 MDT 2000---ghush@rndcs11---/rlu21/proj/t96a/REV/ercrun/Fullchip
Fri Apr  7 09:14:32 MDT 2000---lkeller@rndcs11---/rlu31/proj/y15b/REV/ercrun/y15bQ
Fri Apr  7 12:10:56 MDT 2000---lkeller@rndcs13---/rlu31/proj/y15b/REV/ercrun/y15bQ
Fri Apr  7 15:15:45 MDT 2000---rtaylor@rndcs23---/rlu23/home/cadwork/e85a/ercrun/dTop
Sun Apr  9 07:40:27 MDT 2000---rtaylor@rndcs21---/home/cadwork/e85a/ercrun/bot
Sun Apr  9 07:47:45 MDT 2000---rtaylor@rndcs14---/home/cadwork/e85a/ercrun/dTop
Sun Apr  9 08:08:44 MDT 2000---rtaylor@rndcs15---/home/cadwork/e85a/ercrun/dTop
Tue Apr 11 17:11:15 MDT 2000---ghush@rndcs2---/proj/t96a/REV/ercrun/t96aQ
Wed Apr 12 11:29:08 MDT 2000---ghush@rndcs12---/proj/t96a/REV/ercrun/t96aQ
Thu Apr 13 14:51:44 MDT 2000---bprotz@rndcs13---/proj/y95c/REV/ercrun/y95cQ
Mon Apr 17 15:07:16 MDT 2000---ghush@rndcs8---/proj/t96a/REV/ercrun/t96aQ
Wed Apr 19 08:58:05 MDT 2000---ghush@rndcs20---/proj/t96a/REV/ercrun/t96aQ
Mon Apr 24 09:06:48 MDT 2000---lkeller@rndcs18---/proj/y15b/REV/ercrun/y15bQ
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Mon Apr 24 09:26:34 MDT 2000---ghush@rndcs19---/proj/t96a/REV/ercrun/t96aQ
Wed Apr 26 07:28:21 MDT 2000---lkeller@rndcs5---/proj/y15b/REV/ercrun/y15bQ
Wed Apr 26 14:17:21 MDT 2000---lkeller@rndcs2---/proj/y15b/REV/ercrun/y15bQ
Thu Apr 27 11:27:58 MDT 2000---ghush@rndcs9---/proj/t96a/REV/ercrun/t96aQ
Fri Apr 28 11:34:11 MDT 2000---ghush@rndcs6---/proj/t96a/REV/ercrun/Fullchip
Mon May  1 14:36:43 MDT 2000---ghush@rndcs16---/proj/t96a/REV/ercrun/t96aQ
Wed May  3 14:05:42 MDT 2000---ghush@rndcs21---/proj/t96a/REV/ercrun/Fullchip
Fri May  5 09:53:57 MDT 2000---ghush@rndcs11---/proj/t96a/REV/ercrun/Fullchip
Tue May  9 16:12:58 MDT 2000---ghush@rndcs19---/proj/t96a/REV/ercrun/t96aQ
Mon May 15 10:35:15 MDT 2000---lkeller@rndcs10---/proj/y15b/REV/ercrun/y15bQ
Thu May 18 08:52:37 MDT 2000---kkalpakj@rndcs10---/proj/s93a/REV/ercrun/S97AF
Mon May 22 15:09:38 MDT 2000---kkalpakj@rndcs16---/proj/s93a/REV/ercrun/S97AF
Mon May 22 17:58:06 MDT 2000---kkalpakj@rndcs20---/proj/s93a/REV/ercrun/S97AF
Tue May 23 11:07:37 MDT 2000---kkalpakj@rndcs20---/proj/s93a/REV/ercrun/S97AF
Tue May 23 11:36:40 MDT 2000---ghush@rndcs18---/proj/t96a/REV/ercrun/Fullchip
Tue May 23 14:37:48 MDT 2000---kkalpakj@rndcs7---/proj/s93a/REV/ercrun/S97AF
Mon Jul 10 13:58:31 MDT 2000---kkalpakj@rndcs15---/proj/s91a/REV/ercrun/S91AF
Mon Jul 10 14:58:19 MDT 2000---kkalpakj@rndcs11---/proj/s91a/REV/ercrun/S91AF
Mon Aug 28 07:45:17 MDT 2000---wli@rndcs9---/proj/t96a/REV/ercrun/t96aQ
Fri Sep  1 07:20:32 MDT 2000---wli@rndcs12---/proj/t96a/REV/ercrun/t96aQ
Fri Sep  1 11:49:25 MDT 2000---wli@rndcs8---/proj/t96a/REV/ercrun/t96aQ
Mon Sep 11 10:01:01 MDT 2000---tmerritt@rndcs14---/proj/y97a/REV/ercrun/vp2
Mon Sep 18 15:19:15 MDT 2000---gblodget@rndcs5---/proj/y97a/REV/ercrun/CBR_sim
Mon Sep 18 15:24:25 MDT 2000---gblodget@rndcs26---/proj/y97a/REV/ercrun/CBR_sim
Mon Sep 18 17:02:16 MDT 2000---gblodget@rndcs16---/proj/y97a/REV/ercrun/CBR_sim
Tue Sep 19 08:14:42 MDT 2000---gblodget@rndcs10---/proj/y97a/REV/ercrun/CBR_sim
Tue Sep 19 08:16:39 MDT 2000---gblodget@rndcs10---/proj/y97a/REV/ercrun/CBR_sim
Tue Sep 19 08:25:32 MDT 2000---gblodget@rndcs9---/proj/y97a/REV/ercrun/CBR_sim
Tue Sep 19 14:28:37 MDT 2000---bprotz@rndcs24---/proj/y95c/REV/ercrun/y95cQ
Thu Sep 21 15:38:36 MDT 2000---kkalpakj@rndcs6---/proj/s91a/REV/ercrun/S91AF
Tue Sep 26 13:50:29 MDT 2000---kkalpakj@rndcs10---/proj/s91a/REV/ercrun/S91AF
Wed Sep 27 07:02:05 MDT 2000---wli@rndcs14---/proj/t96a/REV/ercrun/center_periph
Wed Sep 27 09:12:27 MDT 2000---kkalpakj@rndcs22---/proj/s91a/REV/ercrun/S91AF
Tue Oct  3 10:40:05 MDT 2000---kkalpakj@rndcs14---/proj/s91a/REV/ercrun/S91AF
Tue Oct  3 17:22:07 MDT 2000---kkalpakj@rndcs7---/proj/s91a/REV/ercrun/S91AF
Wed Oct  4 13:17:35 MDT 2000---acwei@rndcs12---/proj/t84a/REV/ercrun/t84aQ
Thu Oct 12 15:32:02 MDT 2000---dpinney@rndcs13---/proj/y95w/REV/ercrun/padsup
Thu Oct 12 15:35:48 MDT 2000---dpinney@rndcs10---/proj/y95w/REV/ercrun/padsup
Thu Oct 12 16:20:13 MDT 2000---dpinney@rndcs19---/proj/y95w/REV/ercrun/padsdn
Fri Oct 13 09:13:03 MDT 2000---dpinney@rndcs24---/proj/y95w/REV/ercrun/center
Mon Oct 23 16:21:33 MDT 2000---y95c@rndcs15---/proj/y95c/REV/ercrun/y95cQ
Mon Oct 23 16:33:20 MDT 2000---t85a@rndcs13---/proj/t85a/REV/ercrun/t85aQ
Mon Nov  6 08:02:59 MST 2000---ksdyer@rndcs9---/proj/t85a/REV/ercrun/padsdn
Mon Nov  6 08:10:42 MST 2000---ksdyer@rndcs17---/proj/t85a/REVT22/ercrun/padsdn
Mon Nov  6 12:03:59 MST 2000---t85a@rndcs10---/proj/t85a/REV/ercrun/t85aQ
Mon Nov  6 15:27:38 MST 2000---t85a@rndcs5---/proj/t85a/REV/ercrun/t85aQ
Tue Nov  7 08:36:09 MST 2000---dpinney@rndcs8---/proj/y95w/REV/ercrun/padsup
Tue Nov  7 16:06:34 MST 2000---dpinney@rndcs5---/proj/y95w/REV/ercrun/padsup
Fri Nov 10 10:11:41 MST 2000---lkeller@rndcs26---/proj/y15b/REV/ercrun/y15bQ
Mon Nov 13 07:44:12 MST 2000---dpinney@rndcs7---/proj/y95w/REV/ercrun/y95wQ
Mon Nov 13 10:01:38 MST 2000---dpinney@rndcs17---/proj/y95w/REV/ercrun/center
Mon Nov 13 10:42:56 MST 2000---dpinney@rndcs9---/proj/y95w/REV/ercrun/center
Mon Nov 13 11:02:57 MST 2000---bthompso@rndcs6---/proj/s93a/REV/ercrun/S93AF
Mon Nov 13 11:03:20 MST 2000---jsher@rndcs26---/proj/s93a/REV/ercrun/CENTER_right
Mon Nov 13 11:49:45 MST 2000---lkeller@rndcs20---/proj/y15b/REV/ercrun/y15bQ
Mon Nov 13 13:43:07 MST 2000---dpinney@rndcs2---/proj/y95w/REV/ercrun/array32m
Mon Nov 13 13:58:39 MST 2000---jsher@rndcs14---/proj/s93a/REV/ercrun/CENTER_right
Mon Nov 13 14:00:24 MST 2000---jsher@rndcs18---/proj/s93a/REV/ercrun/CENTER_left
Mon Nov 13 15:28:16 MST 2000---lkeller@rndcs12---/proj/y15b/REVB/ercrun/y15bQ
Mon Nov 13 15:45:18 MST 2000---jsher@rndcs18---/proj/s93a/REV/ercrun/TOP
Mon Nov 13 15:54:27 MST 2000---jsher@rndcs26---/proj/s93a/REV/ercrun/BOTTOM
Mon Nov 13 16:06:01 MST 2000---jsher@rndcs18---/proj/s93a/REV/ercrun/LEFT
Mon Nov 13 17:39:42 MST 2000---jsher@rndcs26---/proj/s93a/REV/ercrun/RIGHT
Mon Nov 13 17:57:00 MST 2000---lkeller@rndcs6---/proj/y15b/REV/ercrun/y15bQ
Tue Nov 14 08:07:42 MST 2000---lkeller@rndcs22---/proj/y15b/REVB/ercrun/y15bQ
Tue Nov 14 09:33:23 MST 2000---jsher@rndcs11---/proj/s93a/REV/ercrun/LEFT
Wed Nov 15 07:41:11 MST 2000---lkeller@rndcs8---/proj/y15b/REVB/ercrun/y15bQ
Wed Nov 15 10:47:44 MST 2000---lkeller@rndcs21---/proj/y15b/REVB/ercrun/y15bQ
Wed Nov 15 17:46:28 MST 2000---lkeller@rndcs7---/proj/y15b/REVB/ercrun/y15bQ
Thu Nov 16 06:15:16 MST 2000---lkeller@rndcs10---/proj/y15b/REVB/ercrun/y15bQ
Thu Nov 16 14:37:37 MST 2000---lkeller@rndcs4---/proj/y15b/REVB/ercrun/y15bQ
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Thu Nov 16 16:47:43 MST 2000---lkeller@rndcs26---/proj/y15b/REVB/ercrun/y15bQ
Thu Nov 16 18:03:41 MST 2000---lkeller@rndcs7---/proj/y15b/REVB/ercrun/y15bQ
Fri Nov 17 09:38:47 MST 2000---dpinney@rndcs10---/proj/y95w/REV/ercrun/padsup
Fri Nov 17 11:36:16 MST 2000---dpinney@rndcs21---/proj/y95w/REV/ercrun/y95wQ
Mon Nov 20 08:53:42 MST 2000---ksdyer@rndcs8---/proj/t85a/REV/ercrun/t85aQ
Mon Nov 20 16:55:42 MST 2000---t85a@rndcs12---/proj/t85a/REV/ercrun/t85aQ
Tue Nov 21 08:21:53 MST 2000---vwong@rndcs5---/proj/y95w/REV/ercrun/y95wQ
Tue Nov 21 09:07:09 MST 2000---vwong@rndcs7---/proj/y95w/REV/ercrun/y95wQ
Tue Nov 21 15:43:25 MST 2000---vwong@rndcs21---/proj/y95w/REV/ercrun/y95wQ
Tue Nov 21 20:12:38 MST 2000---vwong@rndcs14---/proj/y95w/REV/ercrun/y95wQ
Wed Nov 22 07:46:03 MST 2000---vwong@rndcs7---/proj/y95w/REV/ercrun/y95wQ
Wed Nov 22 08:39:03 MST 2000---vwong@rndcs25---/proj/y95w/REV/ercrun/y95wQ
Wed Nov 22 10:11:56 MST 2000---vwong@rndcs5---/proj/y95w/REV/ercrun/y95wQ
Wed Nov 22 11:03:51 MST 2000---vwong@rndcs7---/proj/y95w/REV/ercrun/y95wQ
Wed Nov 22 14:12:55 MST 2000---vwong@rndcs16---/proj/y95w/REV/ercrun/y95wQ
Wed Nov 22 14:22:11 MST 2000---bkeeth@rndcs24---/proj/h96a/REV/ercrun/dataRegP
Wed Nov 22 15:22:05 MST 2000---bkeeth@rndcs4---/proj/h96a/REV/ercrun/gap0
Wed Nov 22 17:00:53 MST 2000---vwong@rndcs22---/proj/y95w/REV/ercrun/y95wQ
Wed Nov 22 17:06:03 MST 2000---vwong@rndcs16---/proj/y95w/REV/ercrun/y95wQ
Wed Nov 22 20:09:52 MST 2000---y95w@rndcs21---/proj/y95w/REV/ercrun/y95wQ
Fri Nov 24 08:14:28 MST 2000---vwong@rndcs18---/proj/y95w/REV/ercrun/y95wQ
Fri Nov 24 08:52:22 MST 2000---vwong@rndcs20---/proj/y95w/REV/ercrun/y95wQ
Mon Nov 27 09:06:15 MST 2000---vwong@rndcs21---/proj/y95w/REV/ercrun/y95wQ
Mon Nov 27 10:58:45 MST 2000---russphil@rndcs14---/proj/h96a/REV/ercrun/vbb.sim
Mon Nov 27 10:59:38 MST 2000---russphil@rndcs26---/proj/h96a/REV/ercrun/vbb.sim
Mon Nov 27 11:02:50 MST 2000---russphil@rndcs22---/proj/h96a/REV/ercrun/vbb_regsel
Mon Nov 27 11:17:21 MST 2000---russphil@rndcs22---/proj/h96a/REV/ercrun/vbb_regsel
Mon Nov 27 11:37:57 MST 2000---russphil@rndcs21---/proj/h96a/REV/ercrun/vbb_osc
Mon Nov 27 11:39:59 MST 2000---russphil@rndcs21---/proj/h96a/REV/ercrun/vbbDiffReg2
Mon Nov 27 11:42:36 MST 2000---russphil@rndcs21---/proj/h96a/REV/ercrun/vbbLogic
Mon Nov 27 13:33:18 MST 2000---russphil@rndcs8---/proj/h96a/REV/ercrun/vbbLogic
Mon Nov 27 15:27:38 MST 2000---russphil@rndcs14---/proj/h96a/REV/ercrun/vbbLogic
Mon Nov 27 15:30:24 MST 2000---russphil@rndcs14---/proj/h96a/REV/ercrun/vbbPump
Mon Nov 27 15:32:31 MST 2000---russphil@rndcs10---/proj/h96a/REV/ercrun/vbb_core_2phase
Mon Nov 27 15:44:01 MST 2000---russphil@rndcs2---/proj/h96a/REV/ercrun/vbbPump
Mon Nov 27 15:46:22 MST 2000---russphil@rndcs26---/proj/h96a/REV/ercrun/vbbPump
Tue Nov 28 09:06:53 MST 2000---russphil@rndcs24---/proj/h96a/REV/ercrun/vbbLogic
Tue Nov 28 09:33:46 MST 2000---tmerritt@rndcs18---/proj/y97a/REV/ercrun/inref_gen
Tue Nov 28 09:37:31 MST 2000---tmerritt@rndcs21---/proj/y97a/REV/ercrun/inref_gen
Tue Nov 28 09:42:57 MST 2000---tmerritt@rndcs23---/proj/y97a/REV/ercrun/inref_gen
Tue Nov 28 09:44:48 MST 2000---tmerritt@rndcs18---/proj/y97a/REV/ercrun/inref_gen
Tue Nov 28 09:48:04 MST 2000---russphil@rndcs2---/proj/h96a/REV/ercrun/vbbLogic
Tue Nov 28 09:50:06 MST 2000---russphil@rndcs15---/proj/h96a/REV/ercrun/vbbLogic
Tue Nov 28 09:59:54 MST 2000---tmerritt@rndcs26---/proj/y97a/REV/ercrun/inref_gen
Tue Nov 28 11:04:40 MST 2000---tmerritt@rndcs24---/proj/y97a/REV/ercrun/inref_gen
Tue Nov 28 13:36:04 MST 2000---jonadams@rndcs17---/proj/y16x/REV/TMOD/REV01/ercrun/test
Tue Nov 28 15:36:22 MST 2000---russphil@rndcs23---/proj/h96a/REV/ercrun/vccp.sim
Tue Nov 28 15:47:45 MST 2000---russphil@rndcs10---/proj/h96a/REV/ercrun/vccpBlkCtrl1
Tue Nov 28 15:52:56 MST 2000---russphil@rndcs22---/proj/h96a/REV/ercrun/vccpBlkCtrl1
Tue Nov 28 16:25:19 MST 2000---russphil@rndcs26---/proj/h96a/REV/ercrun/vccpBlkCtrl1
Tue Nov 28 16:38:17 MST 2000---russphil@rndcs23---/proj/h96a/REV/ercrun/vccpBlkCtrl1
Tue Nov 28 16:39:13 MST 2000---russphil@rndcs5---/proj/h96a/REV/ercrun/vccpDBPumps
Wed Nov 29 10:04:45 MST 2000---russphil@rndcs17---/proj/h96a/REV/ercrun/vccpBlkCtrl1
Wed Nov 29 14:49:29 MST 2000---russphil@rndcs20---/proj/h96a/REV/ercrun/vccpDBPumps
Thu Nov 30 08:03:34 MST 2000---russphil@rndcs14---/proj/h96a/REV/ercrun/vccpDBPumps
Thu Nov 30 08:08:44 MST 2000---russphil@rndcs22---/proj/h96a/REV/ercrun/vccpDiffReg1
Thu Nov 30 08:57:59 MST 2000---russphil@rndcs19---/proj/h96a/REV/ercrun/vccpDBPumps
Thu Nov 30 16:18:16 MST 2000---t85a@rndcs10---/proj/t85a/REV/ercrun/t85aQ
Tue Dec  5 13:29:44 MST 2000---bkeeth@rndcs24---/proj/h96a/REV/ercrun/gap0
Tue Dec  5 13:37:38 MST 2000---bkeeth@rndcs25---/proj/h96a/REV/ercrun/gap0
Tue Dec  5 15:48:57 MST 2000---bkeeth@rndcs18---/proj/h96a/REV/ercrun/gap0
Tue Dec  5 15:57:18 MST 2000---rtaylor@rndcs25---/home/cadwork/h96a/ercrun/gap0
Tue Dec  5 16:02:24 MST 2000---bkeeth@rndcs19---/proj/h96a/REV/ercrun/gap0
Tue Dec  5 16:04:16 MST 2000---bkeeth@rndcs10---/proj/h96a/REV/ercrun/gap0
Tue Dec  5 16:07:56 MST 2000---bkeeth@rndcs4---/proj/h96a/REV/ercrun/gap0
Wed Dec  6 11:00:42 MST 2000---gblodget@rndcs8---/proj/d94b/REV/ercrun/CENTER_CHIP_R
Wed Dec  6 11:39:19 MST 2000---huyvo@rndcs13---/proj/191a/REV_DRAM/ercrun/isoDrvNEW
Wed Dec  6 12:10:44 MST 2000---xxi@rndcs5---/home/cadwork/t96a/ercrun/cmdinlat_r
Wed Dec  6 12:33:39 MST 2000---bprotz@rndcs24---/proj/y97a/REV/ercrun/periph_logic1
Wed Dec  6 12:42:45 MST 2000---bprotz@rndcs19---/proj/y97a/REV/ercrun/qtrcntr
Wed Dec  6 13:02:23 MST 2000---cingalls@rndcs17---/proj/y97a/REV/ercrun/fzRowBlock
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Wed Dec  6 13:10:37 MST 2000---cingalls@rndcs11---/proj/y97a/REV/ercrun/fzRowBlock
Wed Dec  6 13:16:27 MST 2000---huyvo@rndcs11---/proj/l91a/REV_DRAM/ercrun/isoDrvNEW
Wed Dec  6 13:17:22 MST 2000---cingalls@rndcs11---/proj/y97a/REV/ercrun/fzCol4mBlk
Wed Dec  6 13:21:53 MST 2000---huyvo@rndcs15---/proj/l91a/REV_DRAM/ercrun/isoDrvNEW
Wed Dec  6 13:31:44 MST 2000---huyvo@rndcs18---/proj/l91a/REV_DRAM/ercrun/arrdrv128IO
Wed Dec  6 13:35:41 MST 2000---gblodget@rndcs16---/proj/d94b/REV/ercrun/CENTER_CHIP_R
Wed Dec  6 13:36:33 MST 2000---huyvo@rndcs21---/proj/l91a/REV_DRAM/ercrun/botemd2
Wed Dec  6 14:10:35 MST 2000---cingalls@rndcs20---/proj/y97a/REV/ercrun/FuseRCTier
Wed Dec  6 14:32:23 MST 2000---gblodget@rndcs20---/proj/d94b/REV/ercrun/CENTER_CHIP_R
Wed Dec  6 14:46:32 MST 2000---gblodget@rndcs12---/proj/d94b/REV/ercrun/CENTER_CHIP_L
Wed Dec  6 15:14:33 MST 2000---bprotz@rndcs23---/proj/y97a/REV/ercrun/qtrcntr
Wed Dec  6 15:33:50 MST 2000---gblodget@rndcs5---/proj/d94b/REV/ercrun/CENTER_CHIP_L
Wed Dec  6 17:05:37 MST 2000---huyvo@rndcs11---/proj/l91a/REV_DRAM/ercrun/data_blk128IO
Wed Dec  6 17:07:06 MST 2000---huyvo@rndcs22---/proj/l91a/REV_DRAM/ercrun/throat128IO
Wed Dec  6 17:08:02 MST 2000---gblodget@rndcs13---/proj/d94b/REV/ercrun/CENTER_CHIP_L
Wed Dec  6 17:10:23 MST 2000---huyvo@rndcs25---/proj/l91a/REV_DRAM/ercrun/nsq256K
Wed Dec  6 17:12:10 MST 2000---huyvo@rndcs2---/proj/l91a/REV_DRAM/ercrun/rdec128xMid
Wed Dec  6 17:13:56 MST 2000---gblodget@rndcs18---/proj/d94b/REV/ercrun/CENTER_CHIP_L
Wed Dec  6 17:17:27 MST 2000---gblodget@rndcs24---/proj/d94b/REV/ercrun/padQ
Thu Dec  7 05:18:51 MST 2000---cingalls@rndcs12---/proj/y97a/REV/ercrun/fzLogic
Thu Dec  7 06:28:59 MST 2000---cingalls@rndcs17---/proj/y97a/REV/ercrun/fzLogic
Thu Dec  7 06:45:38 MST 2000---cingalls@rndcs25---/proj/y97a/REV/ercrun/fzLogic
Thu Dec  7 06:48:58 MST 2000---cingalls@rndcs10---/proj/y97a/REV/ercrun/FuseTier
Thu Dec  7 09:01:06 MST 2000---cingalls@rndcs16---/proj/y97a/REV/ercrun/pwramp_pair
Thu Dec  7 09:27:45 MST 2000---cingalls@rndcs10---/proj/y97a/REV/ercrun/pwramp_pair
Thu Dec  7 10:08:17 MST 2000---cingalls@rndcs14---/proj/y97a/REV/ercrun/pwramp_pair
Thu Dec  7 10:19:04 MST 2000---cingalls@rndcs21---/proj/y97a/REV/ercrun/pwramp_pair
Thu Dec  7 10:30:16 MST 2000---cingalls@rndcs7---/proj/y97a/REV/ercrun/pwramp_pair
Thu Dec  7 10:32:12 MST 2000---cingalls@rndcs12---/proj/y97a/REV/ercrun/reg_cntrl
Thu Dec  7 11:20:17 MST 2000---cingalls@rndcs2---/proj/y97a/REV/ercrun/regulator
Thu Dec  7 11:25:53 MST 2000---cingalls@rndcs22---/proj/y97a/REV/ercrun/vccxdetect
Thu Dec  7 12:22:05 MST 2000---tmerritt@rndcs10---/proj/y97a/REV/ercrun/ltdrvr
Thu Dec  7 12:27:21 MST 2000---gblodget@rndcs24---/proj/d94b/REV/ercrun/padQ
Thu Dec  7 13:04:44 MST 2000---cingalls@rndcs26---/proj/y97a/REV/ercrun/vccxdetect
Thu Dec  7 13:08:29 MST 2000---cingalls@rndcs7---/proj/y97a/REV/ercrun/vccxdetect
Thu Dec  7 13:09:59 MST 2000---cingalls@rndcs24---/proj/y97a/REV/ercrun/powerup
Thu Dec  7 13:15:05 MST 2000---cingalls@rndcs7---/proj/y97a/REV/ercrun/powerup
Thu Dec  7 13:18:23 MST 2000---cingalls@rndcs9---/proj/y97a/REV/ercrun/vccr_boost
Thu Dec  7 13:20:26 MST 2000---cingalls@rndcs26---/proj/y97a/REV/ercrun/regPwrUp
Thu Dec  7 13:31:28 MST 2000---cingalls@rndcs24---/proj/y97a/REV/ercrun/regPwrUp
Thu Dec  7 14:37:06 MST 2000---gblodget@rndcs13---/proj/d94b/REV/ercrun/CENTER_CHIP
Fri Dec  8 08:19:04 MST 2000---cingalls@rndcs13---/proj/y97a/REV/ercrun/regulator
Fri Dec  8 09:13:40 MST 2000---y15b@rndcs8---/proj/y15b/REV/ercrun/y15bQ
Fri Dec  8 09:29:46 MST 2000---cingalls@rndcs23---/proj/y97a/REV/ercrun/triref
Fri Dec  8 11:54:03 MST 2000---cingalls@rndcs12---/proj/y97a/REV/ercrun/triref
Fri Dec  8 14:16:30 MST 2000---cingalls@rndcs20---/proj/y97a/REV/ercrun/triref
Fri Dec  8 14:21:08 MST 2000---cingalls@rndcs15---/proj/y97a/REV/ercrun/triref
Fri Dec  8 14:40:06 MST 2000---cingalls@rndcs11---/proj/y97a/REV/ercrun/triref
Mon Dec 11 06:27:43 MST 2000---cingalls@rndcs17---/proj/y97a/REV/ercrun/regulator
Mon Dec 11 06:53:34 MST 2000---cingalls@rndcs23---/proj/y97a/REV/ercrun/bankPwrAmp
Mon Dec 11 07:03:25 MST 2000---cingalls@rndcs10---/proj/y97a/REV/ercrun/bankPwrAmp
Mon Dec 11 07:20:00 MST 2000---cingalls@rndcs18---/proj/y97a/REV/ercrun/bankPwrAmp
Mon Dec 11 07:36:45 MST 2000---cingalls@rndcs13---/proj/y97a/REV/ercrun/regulator
Mon Dec 11 08:03:34 MST 2000---cingalls@rndcs7---/proj/y97a/REV/ercrun/regulator
Mon Dec 11 08:20:46 MST 2000---cingalls@rndcs13---/proj/y97a/REV/ercrun/regulator
Mon Dec 11 08:28:31 MST 2000---cingalls@rndcs2---/proj/y97a/REV/ercrun/regulator
Mon Dec 11 14:01:01 MST 2000---cingalls@rndcs13---/proj/y97a/REV/ercrun/regulator
Tue Dec 12 06:08:05 MST 2000---cingalls@rndcs8---/proj/y97a/REV/ercrun/regulator
Tue Dec 12 06:17:32 MST 2000---cingalls@rndcs5---/proj/y97a/REV/ercrun/cgndDrv
Tue Dec 12 06:25:07 MST 2000---cingalls@rndcs9---/proj/y97a/REV/ercrun/cgndDrv
Tue Dec 12 06:28:32 MST 2000---cingalls@rndcs7---/proj/y97a/REV/ercrun/FuseTier
Tue Dec 12 07:28:15 MST 2000---cingalls@rndcs26---/proj/y97a/REV/ercrun/FuseTier
Tue Dec 12 07:49:06 MST 2000---bprotz@rndcs21---/proj/y97a/REV/ercrun/qtrcntr
Tue Dec 12 08:04:44 MST 2000---bprotz@rndcs15---/proj/y97a/REV/ercrun/qtrcntr
Tue Dec 12 08:28:39 MST 2000---bprotz@rndcs8---/proj/y97a/REV/ercrun/qtrcntr
Tue Dec 12 10:23:42 MST 2000---tmerritt@rndcs22---/proj/y97a/REV/ercrun/ltdrvr
Tue Dec 12 10:27:01 MST 2000---tmerritt@rndcs24---/proj/y97a/REV/ercrun/ltdrvr8
Tue Dec 12 10:34:14 MST 2000---tmerritt@rndcs14---/proj/y97a/REV/ercrun/ltdrvr65
Tue Dec 12 10:36:08 MST 2000---tmerritt@rndcs26---/proj/y97a/REV/ercrun/ltdrvr65
Tue Dec 12 10:39:12 MST 2000---tmerritt@rndcs22---/proj/y97a/REV/ercrun/isoDrv2x
Tue Dec 12 10:40:50 MST 2000---tmerritt@rndcs8---/proj/y97a/REV/ercrun/isoDrv2x
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Tue Dec 12 10:41:57 MST 2000---tmerritt@rndcs23---/proj/y97a/REV/ercrun/nsqDrv2x
Tue Dec 12 10:46:49 MST 2000---tmerritt@rndcs10---/proj/y97a/REV/ercrun/throat
Tue Dec 12 11:10:33 MST 2000---bprotz@rndcs23---/proj/y97a/REV/ercrun/qtrcntr
Tue Dec 12 11:48:52 MST 2000---dnorvall@rndcs21---/proj/191a/REV_LOGIC/ercrun/DP1
Wed Dec 13 13:18:48 MST 2000---tmerritt@rndcs19---/proj/y97a/REV/ercrun/inref_ext
Wed Dec 13 13:26:11 MST 2000---graad@rndcs24---/proj/y97a/REV/ercrun/data_out
Wed Dec 13 14:14:50 MST 2000---sbatra@rndcs19---/proj/y97a/REV/ercrun/inref_ext
Wed Dec 13 14:22:35 MST 2000---sbatra@rndcs28---/proj/y97a/REV/ercrun/inref_ext
Wed Dec 13 14:31:43 MST 2000---jcoleman@rndcs14---/proj/191a/REV_DRAM/ercrun/botemd2
Wed Dec 13 15:08:48 MST 2000---jcoleman@rndcs15---/proj/191a/REV_DRAM/ercrun/botemd2
Wed Dec 13 15:27:17 MST 2000---jcoleman@rndcs20---/proj/191a/REV_DRAM/ercrun/botemd2
Wed Dec 13 15:37:19 MST 2000---jcoleman@rndcs14---/proj/191a/REV_DRAM/ercrun/botemd2
Wed Dec 13 15:54:34 MST 2000---jcoleman@rndcs10---/proj/191a/REV_DRAM/ercrun/botemd2
Thu Dec 14 10:21:22 MST 2000---huyvo@rndcs24---/proj/191a/REV_DRAM/ercrun/arqseg256K
Thu Dec 14 10:21:22 MST 2000---ksdyer@rndcs7---/proj/t85a/REVT26/ercrun/t85aQ
Thu Dec 14 10:31:17 MST 2000---huyvo@rndcs28---/proj/191a/REV_DRAM/ercrun/pnsadrvMid
Thu Dec 14 10:33:06 MST 2000---huyvo@rndcs10---/proj/191a/REV_DRAM/ercrun/M8128IO
Thu Dec 14 11:15:25 MST 2000---huyvo@rndcs16---/proj/191a/REV_DRAM/ercrun/nsq256K
Thu Dec 14 14:24:15 MST 2000---jcoleman@rndcs7---/proj/191a/REV_DRAM/ercrun/botemd2
Thu Dec 14 14:40:58 MST 2000---jcoleman@rndcs13---/proj/191a/REV_DRAM/ercrun/botemd2
Thu Dec 14 14:58:09 MST 2000---jcoleman@rndcs9---/proj/191a/REV_DRAM/ercrun/botemd2
Thu Dec 14 17:05:12 MST 2000---ksdyer@rndcs14---/proj/t85a/REV/ercrun/t85aQ
Fri Dec 15 11:08:37 MST 2000---jcoleman@rndcs24---/proj/191a/REV_DRAM/ercrun/botemd2
Fri Dec 15 11:16:39 MST 2000---jcoleman@rndcs17---/proj/191a/REV_DRAM/ercrun/botemd2
Fri Dec 15 11:27:07 MST 2000---jcoleman@rndcs24---/proj/191a/REV_DRAM/ercrun/botemd2
Fri Dec 15 11:33:39 MST 2000---jcoleman@rndcs28---/proj/191a/REV_DRAM/ercrun/botemd2
Fri Dec 15 13:42:32 MST 2000---jcoleman@rndcs17---/proj/191a/REV_DRAM/ercrun/191a_q
Fri Dec 15 13:43:55 MST 2000---jcoleman@rndcs8---/proj/191a/REV_DRAM/ercrun/throat128IO
Fri Dec 15 13:45:00 MST 2000---jcoleman@rndcs7---/proj/191a/REV_DRAM/ercrun/arrdrv128IO
Fri Dec 15 13:48:38 MST 2000---jcoleman@rndcs7---/proj/191a/REV_DRAM/ercrun/arrdrv128IO
Fri Dec 15 13:49:45 MST 2000---jcoleman@rndcs9---/proj/191a/REV_DRAM/ercrun/data_blk128IO
Fri Dec 15 13:56:11 MST 2000---jcoleman@rndcs19---/proj/191a/REV_DRAM/ercrun/M8128IO
Fri Dec 15 13:59:17 MST 2000---jcoleman@rndcs7---/proj/191a/REV_DRAM/ercrun/191a_q
Fri Dec 15 15:24:31 MST 2000---jcoleman@rndcs28---/proj/191a/REV_DRAM/ercrun/191a_q
Tue Dec 19 05:33:39 MST 2000---cingalls@rndcs12---/proj/y97a/REV/ercrun/regulator
Tue Dec 19 06:44:18 MST 2000---cingalls@rndcs8---/proj/y97a/REV/ercrun/mode_control
Tue Dec 19 13:14:53 MST 2000---bprotz@rndcs25---/proj/y97a/REV/ercrun/qtrcntr
Wed Dec 20 12:21:04 MST 2000---cingalls@rndcs28---/proj/y97a/REV/ercrun/regulator
Wed Dec 20 12:28:23 MST 2000---cingalls@rndcs18---/proj/y97a/REV/ercrun/regulator
Wed Dec 20 15:32:42 MST 2000---romer@rndcs22---/proj/y96a/REV/ercrun/y96aQ
Thu Dec 21 08:37:51 MST 2000---russphil@rndcs24---/proj/h96a/REV/ercrun/powerupLogic
Thu Dec 21 08:39:08 MST 2000---russphil@rndcs12---/proj/h96a/REV/ercrun/vbb_osc
Thu Dec 21 12:33:49 MST 2000---jmorris@rndcs12---/proj/y96a/REV/ercrun/array32m
Thu Dec 21 13:14:26 MST 2000---sbatra@rndcs13---/proj/y97a/REV/ercrun/dqdrv
Thu Dec 21 20:36:10 MST 2000---jmorris@rndcs28---/proj/y96a/REV/ercrun/y96aQ
Fri Dec 22 10:29:50 MST 2000---tmerritt@rndcs26---/proj/y97a/REV/ercrun/svdetect1
Fri Dec 22 10:31:18 MST 2000---tmerritt@rndcs25---/proj/y97a/REV/ercrun/supervolt
Fri Dec 22 10:33:05 MST 2000---tmerritt@rndcs7---/proj/y97a/REV/ercrun/supervolt
Fri Dec 22 11:44:50 MST 2000---jmorris@rndcs18---/proj/y96a/REV/ercrun/y96aQ
Fri Dec 22 18:23:34 MST 2000---jmorris@rndcs28---/proj/y96a/REV/ercrun/y96aQ
Wed Dec 27 10:34:27 MST 2000---jkoellin@rndcs18---/proj/y96a/REV/ercrun/y96aQ
Thu Dec 28 13:44:58 MST 2000---graad@rndcs14---/proj/y97a/REV/ercrun/data_out
Thu Dec 28 14:58:53 MST 2000---graad@rndcs18---/proj/y97a/REV/ercrun/data_out
Thu Dec 28 15:08:41 MST 2000---graad@rndcs12---/proj/y97a/REV/ercrun/data_out
Thu Dec 28 16:26:27 MST 2000---graad@rndcs25---/proj/y97a/REV/ercrun/data_out
Thu Dec 28 16:38:47 MST 2000---graad@rndcs9---/proj/y97a/REV/ercrun/data_out
Fri Dec 29 13:24:51 MST 2000---graad@rndcs25---/proj/y97a/REV/ercrun/data_out
Fri Dec 29 13:35:40 MST 2000---graad@rndcs20---/proj/y97a/REV/ercrun/data_out
Fri Dec 29 14:12:58 MST 2000---graad@rndcs25---/proj/y97a/REV/ercrun/data_out
Tue Jan  2 08:52:58 MST 2001---graad@rndcs28---/proj/y97a/REV/ercrun/dq_2x
Tue Jan  2 10:23:32 MST 2001---graad@rndcs23---/proj/y97a/REV/ercrun/data_out
Tue Jan  2 10:28:58 MST 2001---graad@rndcs8---/proj/y97a/REV/ercrun/data_out
Tue Jan  2 10:34:08 MST 2001---graad@rndcs6---/proj/y97a/REV/ercrun/data_out
Tue Jan  2 12:05:21 MST 2001---graad@rndcs19---/proj/y97a/REV/ercrun/data_out
Tue Jan  2 13:15:58 MST 2001---graad@rndcs9---/proj/y97a/REV/ercrun/dq_2x
Tue Jan  2 13:28:20 MST 2001---graad@rndcs12---/proj/y97a/REV/ercrun/dq_2x
Tue Jan  2 13:31:57 MST 2001---graad@rndcs23---/proj/y97a/REV/ercrun/dqs
Tue Jan  2 13:36:48 MST 2001---graad@rndcs17---/proj/y97a/REV/ercrun/dqs
Tue Jan  2 13:43:49 MST 2001---graad@rndcs2---/proj/y97a/REV/ercrun/dqboot
Tue Jan  2 13:48:12 MST 2001---graad@rndcs6---/proj/y97a/REV/ercrun/dqboot
Tue Jan  2 13:50:15 MST 2001---graad@rndcs2---/proj/y97a/REV/ercrun/dqs
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Tue Jan  2 13:59:09 MST 2001---graad@rndcs17---/proj/y97a/REV/ercrun/dqdrv
Tue Jan  2 14:25:47 MST 2001---graad@rndcs9---/proj/y97a/REV/ercrun/dqs
Tue Jan  2 14:44:20 MST 2001---graad@rndcs2---/proj/y97a/REV/ercrun/dqs
Tue Jan  2 14:45:46 MST 2001---graad@rndcs15---/proj/y97a/REV/ercrun/dqs
Tue Jan  2 16:17:06 MST 2001---graad@rndcs12---/proj/y97a/REV/ercrun/dqs
Wed Jan  3 08:56:42 MST 2001---russphil@rndcs21---/proj/h96a/REV/ercrun/vbbLogic2
Wed Jan  3 09:30:57 MST 2001---russphil@rndcs19---/proj/h96a/REV/ercrun/vbbLogic2
Wed Jan  3 09:36:10 MST 2001---russphil@rndcs17---/proj/h96a/REV/ercrun/vbbPump
Wed Jan  3 09:43:56 MST 2001---russphil@rndcs14---/proj/h96a/REV/ercrun/vbbPump
Wed Jan  3 09:45:18 MST 2001---russphil@rndcs24---/proj/h96a/REV/ercrun/substrateGND
Wed Jan  3 09:47:41 MST 2001---russphil@rndcs5---/proj/h96a/REV/ercrun/substrateGND
Wed Jan  3 10:39:58 MST 2001---graad@rndcs28---/proj/y97a/REV/ercrun/data_mask
Wed Jan  3 10:56:15 MST 2001---graad@rndcs14---/proj/y97a/REV/ercrun/data_mask
Wed Jan  3 11:03:54 MST 2001---graad@rndcs13---/proj/y97a/REV/ercrun/data_mask
Wed Jan  3 11:12:07 MST 2001---graad@rndcs14---/proj/y97a/REV/ercrun/data_mask
Wed Jan  3 11:18:34 MST 2001---graad@rndcs21---/proj/y97a/REV/ercrun/inputpad_dqm
Wed Jan  3 11:25:10 MST 2001---graad@rndcs8---/proj/y97a/REV/ercrun/data_mask
Wed Jan  3 11:32:15 MST 2001---graad@rndcs17---/proj/y97a/REV/ercrun/data_mask
Wed Jan  3 11:38:34 MST 2001---graad@rndcs17---/proj/y97a/REV/ercrun/inputpad_A12
Thu Jan  4 09:12:16 MST 2001---t85a@rndcs9---/proj/t85a/REVT26/ercrun/t85aQ
Thu Jan  4 13:00:31 MST 2001---russphil@rndcs12---/proj/h96a/REV/ercrun/substrateGND
Thu Jan  4 13:02:25 MST 2001---russphil@rndcs23---/proj/h96a/REV/ercrun/substrateGND
Thu Jan  4 13:03:35 MST 2001---russphil@rndcs7---/proj/h96a/REV/ercrun/vbb_regsel
Thu Jan  4 13:05:49 MST 2001---russphil@rndcs28---/proj/h96a/REV/ercrun/vbb_regsel2
Thu Jan  4 13:27:36 MST 2001---graad@rndcs10---/proj/y97a/REV/ercrun/data_mask
Thu Jan  4 13:41:15 MST 2001---graad@rndcs12---/proj/y97a/REV/ercrun/data_mask
Thu Jan  4 13:45:13 MST 2001---graad@rndcs14---/proj/y97a/REV/ercrun/data_mask
Thu Jan  4 15:06:03 MST 2001---graad@rndcs25---/proj/y97a/REV/ercrun/pads_all
Thu Jan  4 15:19:55 MST 2001---russphil@rndcs6---/proj/h96a/REV/ercrun/vbb_regsel
Thu Jan  4 15:20:58 MST 2001---russphil@rndcs22---/proj/h96a/REV/ercrun/vbb_regsel
Thu Jan  4 15:22:16 MST 2001---russphil@rndcs14---/proj/h96a/REV/ercrun/vbb_regsel2
Thu Jan  4 15:24:16 MST 2001---russphil@rndcs23---/proj/h96a/REV/ercrun/substrateGND
Thu Jan  4 15:26:02 MST 2001---russphil@rndcs4---/proj/h96a/REV/ercrun/vbb_regsel
Thu Jan  4 15:27:11 MST 2001---russphil@rndcs4---/proj/h96a/REV/ercrun/vbb_regsel
Thu Jan  4 15:28:31 MST 2001---russphil@rndcs6---/proj/h96a/REV/ercrun/vbb_regsel2
Thu Jan  4 17:07:38 MST 2001---graad@rndcs28---/proj/y97a/REV/ercrun/pads_all
Fri Jan  5 15:45:49 MST 2001---graad@rndcs14---/proj/y97a/REV/ercrun/pads_all
Tue Jan  9 14:34:41 MST 2001---bprotz@rndcs26---/proj/y97a/REV/ercrun/qtrcntr
Tue Jan  9 17:23:09 MST 2001---graad@rndcs6---/proj/y97a/REV/ercrun/pads_all
Wed Jan 10 15:37:51 MST 2001---graad@rndcs7---/proj/y97a/REV/ercrun/pads_all
Thu Jan 11 08:58:21 MST 2001---huyvo@rndcs13---/proj/191a/REV_DRAM/ercrun/M8128IOcore
Thu Jan 11 10:45:27 MST 2001---huyvo@rndcs11---/proj/191a/REV_DRAM/ercrun/M8256IODCRcore
Thu Jan 11 11:14:08 MST 2001---huyvo@rndcs14---/proj/191a/REV_DRAM/ercrun/191a_256IODCRq
Thu Jan 11 13:45:01 MST 2001---graad@rndcs22---/proj/y97a/REV/ercrun/pads_all
Thu Jan 11 15:29:33 MST 2001---y96a@rndcs19---/proj/y96a/REV35/ercrun/y96aQ
Fri Jan 12 14:23:34 MST 2001---huyvo@rndcs18---/proj/191a/REV_DRAM/ercrun/191a_q
Fri Jan 12 16:11:12 MST 2001---huyvo@rndcs7---/proj/193a/REV_DRAM/ercrun/M8128IOcore_int
Fri Jan 12 16:39:49 MST 2001---huyvo@rndcs10---/proj/193a/REV_DRAM/ercrun/M8128IOcore_int
Sat Jan 13 12:30:20 MST 2001---y96a@rndcs11---/proj/y96a/REV35/ercrun/padsA_dn
Mon Jan 15 13:37:22 MST 2001---huyvo@rndcs22---/proj/191a/REV_DRAM/ercrun/191a_q
Tue Jan 16 08:05:34 MST 2001---t85a@rndcs2---/proj/t85a/REV/ercrun/t85aQ
Tue Jan 16 12:47:26 MST 2001---tmerritt@rndcs8---/proj/y97a/REV/ercrun/inputpad
Tue Jan 16 12:50:34 MST 2001---tmerritt@rndcs15---/proj/y97a/REV/ercrun/inputpad
Wed Jan 17 13:39:30 MST 2001---huyvo@rndcs9---/proj/191a/REV_DRAM/ercrun/191a_256IODCRq
Thu Jan 18 12:35:02 MST 2001---ksdyer@rndcs10---/proj/t85a/REV/ercrun/t85aQ
Thu Jan 18 17:13:53 MST 2001---huyvo@rndcs8---/proj/191a/REV_DRAM/ercrun/191a_q
Fri Jan 19 13:37:59 MST 2001---huyvo@rndcs9---/proj/191a/REV_DRAM/ercrun/191a_256IODCRq
Mon Jan 22 13:18:45 MST 2001---y95c@rndcs5---/proj/y95c/REV/ercrun/y95cQ
Mon Jan 22 17:23:27 MST 2001---t85a@rndcs7---/proj/t85a/REVT31/ercrun/t85aQ
Tue Jan 23 18:19:29 MST 2001---t85a@rndcs25---/proj/t85a/REVT31/ercrun/t85aQ
Mon Jan 29 08:31:30 MST 2001---jcullum@rndcs5---/proj/y94w/REV/ercrun/padsdn
Wed Jan 31 09:59:35 MST 2001---russphil@rndcs25---/proj/h96a/REV/ercrun/vccp_osc
Wed Jan 31 10:01:14 MST 2001---russphil@rndcs2---/proj/h96a/REV/ercrun/vccpDiffReg2
Wed Jan 31 10:20:54 MST 2001---russphil@rndcs2---/proj/h96a/REV/ercrun/vccpDiffReg2
Wed Jan 31 10:22:29 MST 2001---russphil@rndcs21---/proj/h96a/REV/ercrun/vccp_pullup
Wed Jan 31 10:24:10 MST 2001---russphil@rndcs8---/proj/h96a/REV/ercrun/vccp_dist_clamp
Wed Jan 31 10:35:21 MST 2001---tmerritt@rndcs25---/proj/t17a/REV/ercrun/ltdrvr
Wed Jan 31 10:35:43 MST 2001---russphil@rndcs28---/proj/h96a/REV/ercrun/vccp_dist_clamp
Wed Jan 31 10:36:49 MST 2001---russphil@rndcs28---/proj/h96a/REV/ercrun/vccp_clk_ph_split
Wed Jan 31 10:39:02 MST 2001---russphil@rndcs12---/proj/h96a/REV/ercrun/vccp_core_2step_half
Wed Jan 31 10:40:02 MST 2001---tmerritt@rndcs28---/proj/t17a/REV/ercrun/ltdrvr
```

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Wed Jan 31 10:45:26 MST 2001---russphil@rndcs2---/proj/h96a/REV/ercrun/vccp_core_2step_half
Wed Jan 31 10:46:47 MST 2001---russphil@rndcs26---/proj/h96a/REV/ercrun/vccp_core_2step
Wed Jan 31 10:49:35 MST 2001---russphil@rndcs12---/proj/h96a/REV/ercrun/vccp_core_2step
Wed Jan 31 10:51:28 MST 2001---russphil@rndcs20---/proj/h96a/REV/ercrun/vccpDBPumps
Thu Feb  1 16:40:30 MST 2001---ksdyer@rndcs8---/proj/t85a/REV/ercrun/t85aQ
Mon Feb  5 17:13:53 MST 2001---t85a@rndcs28---/proj/t85a/REV/ercrun/t85aQ
Tue Feb  6 07:01:45 MST 2001---wli@rndcs5---/proj/t96b/REV/ercrun/ar64mg
Tue Feb  6 07:05:40 MST 2001---wli@rndcs16---/proj/t96a/REV/ercrun/ar64mg
Tue Feb  6 08:47:14 MST 2001---wli@rndcs21---/proj/t96a/REV/ercrun/arrdrv_iso
Tue Feb  6 16:08:09 MST 2001---wli@rndcs12---/proj/t96b/REV/ercrun/Fullchip
Wed Feb  7 09:28:02 MST 2001---wli@rndcs6---/proj/t96b/REV/ercrun/Fullchip
Thu Feb  8 08:02:06 MST 2001---ksdyer@rndcs12---/proj/t85a/REV/ercrun/t85aQ
Fri Feb  9 10:44:29 MST 2001---huyvo@rndcs28---/proj/191a/REV_DRAM/ercrun/M8128IOcore
Fri Feb  9 11:35:26 MST 2001---huyvo@rndcs25---/proj/191a/REV_DRAM/ercrun/M8256IODCRcore
Fri Feb  9 12:25:36 MST 2001---wli@rndcs11---/proj/t96b/REV/ercrun/arrdrv_iso
Fri Feb  9 12:27:07 MST 2001---wli@rndcs13---/proj/t96b/REV/ercrun/arrdrv_iso
Mon Feb 12 11:06:25 MST 2001---cmorzano@rndcs11---/proj/u96a/REV/ercrun/Fullchip
Mon Feb 12 15:48:54 MST 2001---cmorzano@rndcs18---/proj/u96a/REV/ercrun/Fullchip
Mon Feb 12 15:54:46 MST 2001---tmerritt@rndcs12---/proj/t17a/REV/ercrun/ltdrvr
Mon Feb 12 15:55:45 MST 2001---tmerritt@rndcs16---/proj/t17a/REV/ercrun/array64meg
Mon Feb 12 15:56:59 MST 2001---tmerritt@rndcs11---/proj/t17a/REV/ercrun/ltdrvr
Mon Feb 12 16:01:29 MST 2001---tmerritt@rndcs25---/proj/t17a/REV/ercrun/ltdrvr65
Mon Feb 12 16:52:45 MST 2001---mthomann@rndcs16---/proj/t96b/REV/ercrun/Fullchip
Tue Feb 13 08:41:30 MST 2001---jcullum@rndcs5---/proj/y94w/REV/ercrun/center
Tue Feb 13 09:30:11 MST 2001---wli@rndcs19---/proj/t96b/REV/ercrun/t96bQ
Tue Feb 13 10:40:02 MST 2001---cmorzano@rndcs5---/proj/u96a/REV/ercrun/Fullchip
Tue Feb 13 14:59:18 MST 2001---cmorzano@rndcs24---/proj/u96a/REV/ercrun/Fullchip
Wed Feb 14 15:20:43 MST 2001---russphil@rndcs11---/proj/h96a/REV/ercrun/vccpDBPumps
Wed Feb 14 15:32:53 MST 2001---russphil@rndcs7---/proj/h96a/REV/ercrun/vccp_core_2step_half
Wed Feb 14 15:43:32 MST 2001---russphil@rndcs14---/proj/h96a/REV/ercrun/vccp_core_2step_half
Mon Feb 19 06:55:26 MST 2001---tmerritt@rndcs21---/proj/t17a/REV/ercrun/phdrvr_gap_edge
Mon Feb 19 06:57:31 MST 2001---tmerritt@rndcs20---/proj/t17a/REV/ercrun/phdrvr_gap_edge
Mon Feb 19 07:04:47 MST 2001---tmerritt@rndcs21---/proj/t17a/REV/ercrun/phdrvr_gap
Mon Feb 19 07:07:13 MST 2001---tmerritt@rndcs10---/proj/t17a/REV/ercrun/phdrvr_gap
Mon Feb 19 07:10:24 MST 2001---tmerritt@rndcs24---/proj/t17a/REV/ercrun/nsqb
Mon Feb 19 07:13:38 MST 2001---tmerritt@rndcs21---/proj/t17a/REV/ercrun/nsq
Mon Feb 19 07:21:20 MST 2001---tmerritt@rndcs11---/proj/t17a/REV/ercrun/wldrvr_edge
Mon Feb 19 07:22:37 MST 2001---tmerritt@rndcs8---/proj/t17a/REV/ercrun/wldrvr
Mon Feb 19 07:26:07 MST 2001---tmerritt@rndcs5---/proj/t17a/REV/ercrun/array32meg
Mon Feb 19 08:30:38 MST 2001---tmerritt@rndcs13---/proj/t17a/REV/ercrun/pnsa4
Mon Feb 19 08:34:00 MST 2001---tmerritt@rndcs20---/proj/t17a/REV/ercrun/array32meg
Mon Feb 19 08:35:30 MST 2001---tmerritt@rndcs13---/proj/t17a/REV/ercrun/isoDrv2x
Mon Feb 19 08:42:38 MST 2001---tmerritt@rndcs5---/proj/t17a/REV/ercrun/isoDrv2x
Mon Feb 19 08:43:46 MST 2001---tmerritt@rndcs11---/proj/t17a/REV/ercrun/throat
Mon Feb 19 09:09:20 MST 2001---tmerritt@rndcs23---/proj/t17a/REV/ercrun/throat
Mon Feb 19 09:25:03 MST 2001---tmerritt@rndcs8---/proj/t17a/REV/ercrun/vccp_pump
Mon Feb 19 09:25:44 MST 2001---tmerritt@rndcs18---/proj/t17a/REV/ercrun/vccp_core
Mon Feb 19 09:55:07 MST 2001---tmerritt@rndcs28---/proj/t17a/REV/ercrun/vccp_reg
Mon Feb 19 10:03:27 MST 2001---tmerritt@rndcs18---/proj/t17a/REV/ercrun/vccp_reg
Mon Feb 19 10:25:11 MST 2001---t85a@rndcs18---/proj/t85a/REV/ercrun/t85aQ
Wed Feb 21 14:30:08 MST 2001---russphil@rndcs20---/proj/h96a/REV/ercrun/vbbLoad2
Wed Feb 21 17:54:09 MST 2001---russphil@rndcs25---/proj/h96a/REV/ercrun/vbbLoadQuarter
Wed Feb 21 18:03:48 MST 2001---russphil@rndcs15---/proj/h96a/REV/ercrun/vccpLoad
Thu Feb 22 07:23:22 MST 2001---bprotz@rndcs28---/proj/t17a/REV/ercrun/qtrcntr
Thu Feb 22 08:28:11 MST 2001---bprotz@rndcs19---/proj/t17a/REV/ercrun/qtrcntr
Thu Feb 22 08:29:43 MST 2001---russphil@rndcs17---/proj/h96a/REV/ercrun/vbbLoadQuarter
Wed Feb 28 14:45:39 MST 2001---wli@rndcs14---/proj/t96a/REV/ercrun/arrdrv_iso
Wed Feb 28 14:58:32 MST 2001---wli@rndcs14---/proj/t96a/REV/ercrun/t96aQ
Fri Mar  2 07:48:54 MST 2001---wli@rndcs22---/proj/t96b/REV/ercrun/t96bQ
Thu Mar  8 09:09:23 MST 2001---kkalpakj@rndcs2---/proj/s91a/REV/ercrun/S91AF
Fri Mar  9 09:59:47 MST 2001---gblodget@rndcs20---/proj/t17a/REV/ercrun/CBR_sim
Fri Mar  9 11:37:24 MST 2001---wli@rndcs16---/proj/t96b/REV/ercrun/avc2_dvc2_pad
Fri Mar  9 11:38:51 MST 2001---wli@rndcs9---/proj/t96b/REV/ercrun/avc2_dvc2_pad
Fri Mar  9 11:41:49 MST 2001---wli@rndcs16---/proj/t96b/REV/ercrun/avc2_dvc2_pad
Fri Mar  9 16:27:20 MST 2001---gblodget@rndcs11---/proj/t17a/REV/ercrun/CBR_sim
Mon Mar 12 09:03:38 MST 2001---kkalpakj@rndcs2---/proj/s91a/REV/ercrun/S91AF
Mon Mar 12 10:16:40 MST 2001---kkalpakj@rndcs20---/proj/s91a/REV/ercrun/S91AF
Wed Mar 14 10:03:04 MST 2001---russphil@rndcs29---/proj/xa5a/REV/ercrun/vbbLogic2
Wed Mar 14 10:06:47 MST 2001---russphil@rndcs10---/proj/xa5a/REV/ercrun/vbbLogic2
Wed Mar 14 10:09:22 MST 2001---russphil@rndcs2---/proj/xa5a/REV/ercrun/vbbLogic2
Wed Mar 14 10:09:56 MST 2001---russphil@rndcs14---/proj/xa5a/REV/ercrun/vbbLogic2
```

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Wed Mar 14 11:40:45 MST 2001---russphil@rndcs7---/proj/h96a/REV/ercrun/vccpLBlkCtrl
Wed Mar 14 11:44:37 MST 2001---russphil@rndcs7---/proj/h96a/REV/ercrun/vccpLBlkCtrl
Wed Mar 14 11:49:28 MST 2001---russphil@rndcs10---/proj/h96a/REV/ercrun/vccpLBlkCtrl
Wed Mar 14 11:55:45 MST 2001---russphil@rndcs13---/proj/xa5a/REV/ercrun/vccpLBlkCtrl
Tue Mar 20 07:50:03 MST 2001---kkalpakj@rndcs8---/proj/s91a/REV/ercrun/S91AF
Wed Mar 21 07:18:28 MST 2001---jcullum@rndcs8---/proj/y94w/REV/ercrun/padsdn
Wed Mar 21 07:20:41 MST 2001---jcullum@rndcs6---/proj/y94w/REV/ercrun/padsup
Wed Mar 21 07:20:47 MST 2001---jcullum@rndcs2---/proj/y94w/REV/ercrun/center
Wed Mar 21 11:16:04 MST 2001---wli@rndcs7---/proj/t96b/REV/ercrun/t96bQ
Thu Mar 22 08:56:16 MST 2001---jcullum@rndcs22---/proj/y94w/REV/ercrun/padsdn
Thu Mar 22 12:31:58 MST 2001---jcullum@rndcs13---/proj/y94w/REV/ercrun/vrgen
Thu Mar 22 12:32:18 MST 2001---jcullum@rndcs7---/proj/y94w/REV/ercrun/vrgen
Thu Mar 22 12:33:08 MST 2001---jcullum@rndcs8---/proj/y94w/REV/ercrun/vrgen
Thu Mar 22 12:34:23 MST 2001---jcullum@rndcs2---/proj/y94w/REV/ercrun/vrgen
Thu Mar 22 12:35:16 MST 2001---jcullum@rndcs6---/proj/y94w/REV/ercrun/vrgen
Thu Mar 22 12:37:48 MST 2001---jcullum@rndcs18---/proj/y94w/REV/ercrun/vrgen
Thu Mar 22 13:14:23 MST 2001---jcullum@rndcs6---/proj/y94w/REV/ercrun/padsdn
Thu Mar 22 13:30:20 MST 2001---jcullum@rndcs8---/proj/y94w/REV/ercrun/padsdn
Thu Mar 22 13:34:18 MST 2001---jcullum@rndcs19---/proj/y94w/REV/ercrun/center
Thu Mar 22 13:42:13 MST 2001---jcullum@rndcs26---/proj/y94w/REV/ercrun/padsdn
Thu Mar 22 13:59:59 MST 2001---jcullum@rndcs18---/proj/y94w/REV/ercrun/center
Thu Mar 22 17:31:25 MST 2001---jcullum@rndcs7---/proj/y94w/REV/ercrun/padsdn
Thu Mar 22 17:32:55 MST 2001---jcullum@rndcs7---/proj/y94w/REV/ercrun/center
Fri Mar 23 07:44:55 MST 2001---jcullum@rndcs7---/proj/y94w/REV/ercrun/padsdn
Fri Mar 23 08:52:04 MST 2001---jcullum@rndcs2---/proj/y94w/REV/ercrun/padsdn
Fri Mar 23 10:46:19 MST 2001---jcullum@rndcs22---/proj/y94w/REV/ercrun/padsdn
Fri Mar 23 10:47:22 MST 2001---jcullum@rndcs14---/proj/y94w/REV/ercrun/padsup
Fri Mar 23 10:49:24 MST 2001---jcullum@rndcs19---/proj/y94w/REV/ercrun/center
Fri Mar 23 13:27:58 MST 2001---jcullum@rndcs7---/proj/y94w/REV/ercrun/padsup
Fri Mar 23 13:55:24 MST 2001---jcullum@rndcs18---/proj/y94w/REV/ercrun/padsup
Fri Mar 23 14:23:06 MST 2001---jcullum@rndcs7---/proj/y94w/REV/ercrun/padsup
Fri Mar 23 14:49:00 MST 2001---jcullum@rndcs28---/proj/y94w/REV/ercrun/padsup
Fri Mar 23 15:17:57 MST 2001---jcullum@rndcs22---/proj/y94w/REV/ercrun/padsup
Fri Mar 23 15:29:34 MST 2001---jcullum@rndcs17---/proj/y94w/REV/ercrun/padsup
Fri Mar 23 15:47:14 MST 2001---jcullum@rndcs2---/proj/y94w/REV/ercrun/padsup
Wed Mar 28 10:33:46 MST 2001---sbatra@rndcs7---/proj/t17a/REV/ercrun/dqdrv
Wed Mar 28 11:38:46 MST 2001---sbatra@rndcs23---/proj/t17a/REV/ercrun/dqdrv
Wed Mar 28 11:48:03 MST 2001---sbatra@rndcs7---/proj/t17a/REV/ercrun/dqboot
Wed Mar 28 21:23:05 MST 2001---sbatra@rndcs10---/proj/t17a/REV/ercrun/qslat
Wed Mar 28 13:27:07 MST 2001---sbatra@rndcs22---/proj/t17a/REV/ercrun/doutreg_new
Wed Mar 28 21:28:39 MST 2001---sbatra@rndcs10---/proj/t17a/REV/ercrun/diff_ibuff_datain
Wed Mar 28 15:25:12 MST 2001---sbatra@rndcs2---/proj/t17a/REV/ercrun/dqboot
Wed Mar 28 18:22:00 MST 2001---huyvo@rndcs8---/proj/x84a/REV_DRAM/ercrun/botemd2
Wed Mar 28 18:44:25 MST 2001---huyvo@rndcs8---/proj/x84a/REV_DRAM/ercrun/edram90_q
Thu Mar 29 09:37:01 MST 2001---sbatra@rndcs15---/proj/t17a/REV/ercrun/dqdrv
Thu Mar 29 10:20:20 MST 2001---sbatra@rndcs13---/proj/t17a/REV/ercrun/dqboot
Thu Mar 29 10:21:56 MST 2001---sbatra@rndcs9---/proj/t17a/REV/ercrun/data_in
Mon Apr  2 17:48:45 MDT 2001---t85a@rndcs28---/proj/t85a/REV/ercrun/t85aQ
Mon Apr  2 20:23:47 MDT 2001---t85a@rndcs12---/proj/t85a/REV/ercrun/t85aQ
Tue Apr  3 14:18:58 MDT 2001---cingalls@rndcs2---/proj/t17a/REV/ercrun/ref
Tue Apr  3 14:28:49 MDT 2001---cingalls@rndcs19---/proj/t17a/REV/ercrun/ref
Tue Apr  3 14:30:15 MDT 2001---cingalls@rndcs28---/proj/t17a/REV/ercrun/ref
Tue Apr  3 14:38:49 MDT 2001---cingalls@rndcs17---/proj/t17a/REV/ercrun/regrefn
Tue Apr  3 14:41:22 MDT 2001---cingalls@rndcs2---/proj/t17a/REV/ercrun/regrefn
Tue Apr  3 14:42:57 MDT 2001---cingalls@rndcs19---/proj/t17a/REV/ercrun/regrefn
Tue Apr  3 14:49:22 MDT 2001---cingalls@rndcs9---/proj/t17a/REV/ercrun/triref
Wed Apr  4 12:25:28 MDT 2001---wli@rndcs11---/proj/t96b/REV/ercrun/fzMRGN
Thu Apr  5 08:53:13 MDT 2001---bprotz@rndcs8---/proj/t17a/REV/ercrun/qtrcntr
Thu Apr  5 13:43:34 MDT 2001---cingalls@rndcs15---/proj/t17a/REV/ercrun/pwramp_pair
Thu Apr  5 13:53:23 MDT 2001---cingalls@rndcs19---/proj/t17a/REV/ercrun/pwramp_pair
Thu Apr  5 14:40:27 MDT 2001---cingalls@rndcs28---/proj/t17a/REV/ercrun/boostamp
Thu Apr  5 15:22:54 MDT 2001---sbatra@rndcs11---/proj/t17a/REV/ercrun/data_in
Thu Apr  5 15:23:53 MDT 2001---sbatra@rndcs28---/proj/t17a/REV/ercrun/data_in
Thu Apr  5 15:45:52 MDT 2001---sbatra@rndcs2---/proj/t17a/REV/ercrun/data_in
Thu Apr  5 15:52:36 MDT 2001---sbatra@rndcs20---/proj/t17a/REV/ercrun/data_in
Thu Apr  5 15:55:11 MDT 2001---sbatra@rndcs2---/proj/t17a/REV/ercrun/data_in
Thu Apr  5 15:58:11 MDT 2001---sbatra@rndcs10---/proj/t17a/REV/ercrun/data_in
Thu Apr  5 15:59:54 MDT 2001---sbatra@rndcs10---/proj/t17a/REV/ercrun/data_in
Thu Apr  5 16:23:07 MDT 2001---sbatra@rndcs9---/proj/t17a/REV/ercrun/dq_2x
Thu Apr  5 16:45:55 MDT 2001---sbatra@rndcs11---/proj/t17a/REV/ercrun/data_in
Thu Apr  5 16:47:40 MDT 2001---sbatra@rndcs12---/proj/t17a/REV/ercrun/data_in
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Thu Apr  5 16:49:22 MDT 2001---sbatra@rndcs10---/proj/t17a/REV/ercrun/dq_2x
Thu Apr  5 17:58:34 MDT 2001---t85a@rndcs11---/proj/t85a/REV/ercrun/t85aQ
Fri Apr  6 05:31:28 MDT 2001---cingalls@rndcs12---/proj/t17a/REV/ercrun/bankPwrAmp
Fri Apr  6 06:17:02 MDT 2001---cingalls@rndcs12---/proj/t17a/REV/ercrun/regPwrUp
Fri Apr  6 06:33:41 MDT 2001---cingalls@rndcs2---/proj/t17a/REV/ercrun/bankPwrAmp
Fri Apr  6 06:40:02 MDT 2001---cingalls@rndcs7---/proj/t17a/REV/ercrun/triref
Fri Apr  6 06:53:13 MDT 2001---cingalls@rndcs13---/proj/t17a/REV/ercrun/bandgap
Fri Apr  6 08:22:05 MDT 2001---cingalls@rndcs13---/proj/t17a/REV/ercrun/bandgap
Fri Apr  6 08:30:32 MDT 2001---cingalls@rndcs12---/proj/t17a/REV/ercrun/triref
Fri Apr  6 08:41:35 MDT 2001---cingalls@rndcs25---/proj/t17a/REV/ercrun/triref
Fri Apr  6 11:57:58 MDT 2001---cingalls@rndcs12---/proj/t17a/REV/ercrun/triref
Fri Apr  6 11:59:26 MDT 2001---cingalls@rndcs29---/proj/t17a/REV/ercrun/regulator
Fri Apr  6 12:23:13 MDT 2001---sbatra@rndcs9---/proj/t17a/REV/ercrun/dq_2x
Fri Apr  6 13:02:35 MDT 2001---cmartin@rndcs25---/proj/xa5a/REV/ercrun/padTier_PP
Fri Apr  6 14:11:23 MDT 2001---sbatra@rndcs9---/proj/t17a/REV/ercrun/dq_2x
Fri Apr  6 14:34:30 MDT 2001---sbatra@rndcs23---/proj/t17a/REV/ercrun/dq_2x
Fri Apr  6 14:45:10 MDT 2001---cmartin@rndcs28---/proj/xa5a/REV/ercrun/upperPeriph
Fri Apr  6 16:50:40 MDT 2001---sbatra@rndcs6---/proj/t17a/REV/ercrun/dq_2x
Fri Apr  6 17:01:37 MDT 2001---sbatra@rndcs2---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 05:34:45 MDT 2001---cingalls@rndcs29---/proj/t17a/REV/ercrun/regulator
Mon Apr  9 05:45:51 MDT 2001---cingalls@rndcs23---/proj/t17a/REV/ercrun/GlbFuseTier
Mon Apr  9 09:58:53 MDT 2001---sbatra@rndcs12---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 10:01:41 MDT 2001---cingalls@rndcs15---/proj/t17a/REV/ercrun/regulator
Mon Apr  9 10:18:57 MDT 2001---cingalls@rndcs23---/proj/t17a/REV/ercrun/GlbFuseTier
Mon Apr  9 10:32:03 MDT 2001---cingalls@rndcs18---/proj/t17a/REV/ercrun/GlbFuseTier
Mon Apr  9 10:45:22 MDT 2001---sbatra@rndcs13---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 10:57:05 MDT 2001---sbatra@rndcs12---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 11:07:37 MDT 2001---sbatra@rndcs28---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 11:25:01 MDT 2001---sbatra@rndcs11---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 11:36:37 MDT 2001---sbatra@rndcs8---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 11:46:55 MDT 2001---sbatra@rndcs28---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 12:01:21 MDT 2001---sbatra@rndcs2---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 12:21:34 MDT 2001---sbatra@rndcs8---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 12:34:54 MDT 2001---sbatra@rndcs2---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 15:09:49 MDT 2001---sbatra@rndcs8---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 16:09:52 MDT 2001---sbatra@rndcs24---/proj/t17a/REV/ercrun/dq_2x
Mon Apr  9 17:29:09 MDT 2001---sbatra@rndcs6---/proj/t17a/REV/ercrun/dq_all
Tue Apr 10 15:23:17 MDT 2001---sbatra@rndcs11---/proj/t17a/REV/ercrun/dq_all
Thu Apr 12 15:41:52 MDT 2001---sbatra@rndcs12---/proj/t17a/REV/ercrun/pads_all
Mon Apr 16 10:24:48 MDT 2001---sbatra@rndcs7---/proj/t17a/REV/ercrun/pads_all
Mon Apr 16 16:08:09 MDT 2001---russphil@rndcs11---/proj/xa5a/REV/ercrun/vccpPumpPMOS
Mon Apr 16 16:17:50 MDT 2001---russphil@rndcs12---/proj/xa5a/REV/ercrun/vccpPumpPMOS
Mon Apr 16 16:24:03 MDT 2001---russphil@rndcs7---/proj/xa5a/REV/ercrun/vccpPumpPMOS
Mon Apr 16 16:26:35 MDT 2001---russphil@rndcs8---/proj/xa5a/REV/ercrun/vccpPumpPMOS
Tue Apr 17 09:46:12 MDT 2001---russphil@rndcs23---/proj/xa5a/REV/ercrun/vccpPumpPMOS
Tue Apr 17 10:01:30 MDT 2001---sbatra@rndcs9---/proj/t17a/REV/ercrun/pads_all
Tue Apr 17 10:31:49 MDT 2001---t17a@rndcs7---/proj/t17a/REV/ercrun/pads_all
Tue Apr 17 13:05:11 MDT 2001---sbatra@rndcs13---/proj/t17a/REV/ercrun/pads_all
Tue Apr 17 15:49:21 MDT 2001---sbatra@rndcs9---/proj/t17a/REV/ercrun/pads_all
Tue Apr 17 15:54:40 MDT 2001---sbatra@rndcs8---/proj/t17a/REV/ercrun/pads_all
Wed Apr 18 11:00:01 MDT 2001---sbatra@rndcs13---/proj/t17a/REV/ercrun/pads_all
Wed Apr 18 11:47:13 MDT 2001---sbatra@rndcs6---/proj/t17a/REV/ercrun/pads_all
Wed Apr 18 15:49:53 MDT 2001---sbatra@rndcs17---/proj/t17a/REV/ercrun/pads_all
Wed Apr 18 16:42:04 MDT 2001---sbatra@rndcs17---/proj/t17a/REV/ercrun/pads_all
Thu Apr 19 10:31:51 MDT 2001---sbatra@rndcs13---/proj/t17a/REV/ercrun/pads_all
Thu Apr 19 12:30:31 MDT 2001---sbatra@rndcs11---/proj/t17a/REV/ercrun/pads_all
Fri Apr 20 11:43:08 MDT 2001---sbatra@rndcs8---/proj/t17a/REV/ercrun/pads_all
Fri Apr 20 13:41:08 MDT 2001---sbatra@rndcs26---/proj/t17a/REV/ercrun/pads_all
Fri Apr 20 14:52:54 MDT 2001---sbatra@rndcs13---/proj/t17a/REV/ercrun/pads_all
Mon Apr 23 11:38:33 MDT 2001---wli@rndcs9---/proj/t96b/REV/ercrun/center_periph
Mon Apr 23 15:00:57 MDT 2001---wli@rndcs4---/proj/t96b/REV/ercrun/vbb_core_2ph_t96a
Mon Apr 23 15:03:20 MDT 2001---wli@rndcs11---/proj/t96b/REV/ercrun/vbb_core_2ph_t96a
Mon Apr 23 15:05:30 MDT 2001---wli@rndcs20---/proj/t96b/REV/ercrun/vbb_core_2ph_t96a
Mon Apr 23 15:06:34 MDT 2001---wli@rndcs23---/proj/t96b/REV/ercrun/vbb_core_2ph_t96a
Mon Apr 23 15:39:27 MDT 2001---wli@rndcs20---/proj/t96b/REV/ercrun/center_periph
Tue Apr 24 07:07:39 MDT 2001---wli@rndcs12---/proj/t96b/REV/ercrun/center_periph
Tue Apr 24 08:28:42 MDT 2001---wli@rndcs28---/proj/t96b/REV/ercrun/center_periph
Tue Apr 24 08:58:57 MDT 2001---wli@rndcs6---/proj/t96b/REV/ercrun/center_periph
Tue Apr 24 10:02:33 MDT 2001---wli@rndcs4---/proj/t96b/REV/ercrun/center_periph
Fri Apr 27 17:14:14 MDT 2001---dnorvall@rndcs10---/proj/191a/REV_LOGIC/ercrun/SRAM_Q
Mon Apr 30 11:25:18 MDT 2001---dnorvall@rndcs18---/proj/191a/REV_LOGIC/ercrun/SRAM_Q
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Mon Apr 30 11:39:46 MDT 2001---dnorvall@rndcs2---/proj/191a/REV_LOGIC/ercrun/SRAM_Q
Mon Apr 30 18:29:56 MDT 2001---dnorvall@rndcs13---/proj/191a/REV_LOGIC/ercrun/SRAM_Q
Mon Apr 30 20:33:35 MDT 2001---dnorvall@rndcs19---/proj/191a/REV_LOGIC/ercrun/SRAM_Q
Wed May  2 10:20:39 MDT 2001---jcullum@rndcs7---/proj/y94w/REV/ercrun/fzPsdCgnd
Wed May  2 13:40:09 MDT 2001---dnorvall@rndcs17---/proj/191a/REV_LOGIC/ercrun/SRAM_Q
Wed May  2 13:47:48 MDT 2001---dnorvall@rndcs17---/proj/191a/REV_LOGIC/ercrun/DP1_Q
Wed May  2 13:52:53 MDT 2001---dnorvall@rndcs2---/proj/191a/REV_LOGIC/ercrun/DP1_Q
Wed May  2 16:43:53 MDT 2001---dnorvall@rndcs2---/proj/191a/REV_LOGIC/ercrun/TAG_Q
Thu May  3 12:32:48 MDT 2001---dnorvall@rndcs5---/proj/191a/REV_LOGIC/ercrun/TAG_Q
Thu May  3 14:19:23 MDT 2001---t85a@rndcs19---/proj/t85a/REV/ercrun/t85aQ
Fri May 11 15:37:08 MDT 2001---jcullum@rndcs23---/proj/y94w/REV/ercrun/inbuf_res_m1_diode
Fri May 11 16:34:47 MDT 2001---cmartin@rndcs7---/proj/xa5a/REV/ercrun/periphDLLCAP
Mon May 14 10:05:44 MDT 2001---russphil@rndcs2---/proj/xa5a/REV/ercrun/vccpDBPumps
Mon May 14 10:07:37 MDT 2001---russphil@rndcs10---/proj/xa5a/REV/ercrun/vccp_core_2step_half
Mon May 14 10:16:12 MDT 2001---russphil@rndcs26---/proj/xa5a/REV/ercrun/vccp_core_2step_half
Mon May 14 10:16:49 MDT 2001---russphil@rndcs17---/proj/xa5a/REV/ercrun/vccpDBPumps
Mon May 14 10:28:52 MDT 2001---russphil@rndcs16---/proj/xa5a/REV/ercrun/vccpDBPumps
Mon May 14 10:45:56 MDT 2001---russphil@rndcs18---/proj/xa5a/REV/ercrun/vccpBlkCtrl1
Mon May 14 11:14:05 MDT 2001---russphil@rndcs2---/proj/xa5a/REV/ercrun/vccpBlkCtrl1
Mon May 14 11:16:37 MDT 2001---russphil@rndcs13---/proj/xa5a/REV/ercrun/vccpBlkCtrl1
Mon May 14 11:19:31 MDT 2001---russphil@rndcs24---/proj/xa5a/REV/ercrun/vccpBlkCtrl1
Mon May 14 11:40:51 MDT 2001---russphil@rndcs17---/proj/xa5a/REV/ercrun/vbbPump
Mon May 14 11:53:47 MDT 2001---russphil@rndcs2---/proj/xa5a/REV/ercrun/vbbPump
Mon May 14 12:47:34 MDT 2001---russphil@rndcs17---/proj/xa5a/REV/ercrun/vbb_regsel2
Mon May 14 12:48:49 MDT 2001---russphil@rndcs21---/proj/xa5a/REV/ercrun/vbb_osc
Mon May 14 12:50:20 MDT 2001---russphil@rndcs25---/proj/xa5a/REV/ercrun/vbbDiffReg2
Mon May 14 13:02:09 MDT 2001---russphil@rndcs11---/proj/xa5a/REV/ercrun/vbbLoadQuarter
Mon May 14 13:57:32 MDT 2001---russphil@rndcs11---/proj/xa5a/REV/ercrun/vbb_osc
Mon May 14 13:58:32 MDT 2001---russphil@rndcs18---/proj/xa5a/REV/ercrun/vbbLogic2
Mon May 14 13:59:51 MDT 2001---russphil@rndcs28---/proj/xa5a/REV/ercrun/vccpBlkCtrl1
Mon May 14 14:08:48 MDT 2001---russphil@rndcs13---/proj/xa5a/REV/ercrun/vccpDBPumps
Mon May 14 14:13:29 MDT 2001---russphil@rndcs7---/proj/xa5a/REV/ercrun/vccpDBPumps
Mon May 14 14:16:07 MDT 2001---russphil@rndcs13---/proj/xa5a/REV/ercrun/vccpDBPumps
Mon May 14 14:17:15 MDT 2001---russphil@rndcs2---/proj/xa5a/REV/ercrun/vccpGroup
Mon May 14 14:19:02 MDT 2001---russphil@rndcs16---/proj/xa5a/REV/ercrun/vccpGroup2
Mon May 14 15:07:46 MDT 2001---russphil@rndcs11---/proj/xa5a/REV/ercrun/vccpGroup2
Mon May 14 15:12:19 MDT 2001---russphil@rndcs10---/proj/xa5a/REV/ercrun/vccpGroup3
Mon May 14 16:19:22 MDT 2001---russphil@rndcs13---/proj/xa5a/REV/ercrun/vccpPump10nMNS
Tue May 15 10:28:35 MDT 2001---russphil@rndcs28---/proj/xa5a/REV/ercrun/vccpPump10nMNS
Tue May 15 10:33:27 MDT 2001---russphil@rndcs14---/proj/xa5a/REV/ercrun/vccpPump10nMNS
Tue May 15 14:40:41 MDT 2001---cmartin@rndcs22---/proj/xa5a/REV/ercrun/periphDLLCAP
Tue May 15 15:02:19 MDT 2001---cmartin@rndcs19---/proj/xa5a/REV/ercrun/periphDLLCAP
Tue May 15 15:12:32 MDT 2001---cmartin@rndcs19---/proj/xa5a/REV/ercrun/periphDLLCAP
Tue May 15 15:26:00 MDT 2001---cmartin@rndcs8---/proj/xa5a/REV/ercrun/periphDLLCAP
Tue May 15 15:45:00 MDT 2001---cmartin@rndcs14---/proj/xa5a/REV/ercrun/periphDLLCAP
Tue May 15 16:06:42 MDT 2001---cmartin@rndcs6---/proj/xa5a/REV/ercrun/periphDLLCAP
Tue May 15 16:49:34 MDT 2001---russphil@rndcs6---/proj/xa5a/REV/ercrun/powerupLogic
Wed May 16 09:22:50 MDT 2001---russphil@rndcs11---/proj/xa5a/REV/ercrun/vccpPumpPMOS
Wed May 16 10:40:25 MDT 2001---russphil@rndcs13---/proj/xa5a/REV/ercrun/powerupLogic
Wed May 16 10:41:48 MDT 2001---russphil@rndcs2---/proj/xa5a/REV/ercrun/vccpPumpPMOS
Wed May 16 10:48:28 MDT 2001---russphil@rndcs13---/proj/xa5a/REV/ercrun/vccpPumpPMOS
Wed May 16 11:39:51 MDT 2001---russphil@rndcs11---/proj/xa5a/REV/ercrun/vccpPump10nMNS
Wed May 16 16:16:16 MDT 2001---hbutler@rndcs21---/proj/y25a/REV/ercrun/booted_n_core
Wed May 16 16:23:51 MDT 2001---hbutler@rndcs29---/proj/y25a/REV/ercrun/booted_n_core
Wed May 16 16:25:23 MDT 2001---hbutler@rndcs13---/proj/y25a/REV/ercrun/booted_n_core
Thu May 17 13:11:52 MDT 2001---hbutler@rndcs6---/proj/y25a/REV/ercrun/booted_n_core
Thu May 17 13:14:53 MDT 2001---hbutler@rndcs2---/proj/y25a/REV/ercrun/booted_n_core
Thu May 17 13:15:59 MDT 2001---hbutler@rndcs8---/proj/y25a/REV/ercrun/booted_n_core
Fri May 18 08:59:35 MDT 2001---hbutler@rndcs10---/proj/y25a/REV/ercrun/vccp_p_core
Fri May 18 09:11:00 MDT 2001---hbutler@rndcs28---/proj/y25a/REV/ercrun/3ph_osc
Fri May 18 09:12:13 MDT 2001---hbutler@rndcs8---/proj/y25a/REV/ercrun/p_pumps
Mon May 21 13:13:09 MDT 2001---jsher@rndcs14---/proj/s93a/REV/ercrun/CENTER_left
Mon May 21 16:52:45 MDT 2001---jsher@rndcs23---/proj/s93a/REV/ercrun/CENTER_right
Mon May 21 16:54:04 MDT 2001---jsher@rndcs24---/proj/s93a/REV/ercrun/CENTER_left
Mon May 21 16:54:11 MDT 2001---jsher@rndcs14---/proj/s93a/REV/ercrun/LEFT
Mon May 21 16:55:20 MDT 2001---jsher@rndcs14---/proj/s93a/REV/ercrun/RIGHT
Mon May 21 16:58:50 MDT 2001---jsher@rndcs7---/proj/s93a/REV/ercrun/TOP
Mon May 21 16:59:15 MDT 2001---jsher@rndcs26---/proj/s93a/REV/ercrun/BOTTOM
Wed May 23 12:52:52 MDT 2001---ksdyer@rndcs13---/proj/t85a/REV/ercrun/t85aQ
Thu May 24 15:12:40 MDT 2001---jsher@rndcs13---/proj/s93a/REV/ercrun/spareLeft
Thu May 24 15:12:46 MDT 2001---jsher@rndcs28---/proj/s93a/REV/ercrun/spareRight
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Thu May 24 17:30:39 MDT 2001---jsher@rndcs19---/proj/s93a/REV/ercrun/CENTER
Wed May 30 13:26:59 MDT 2001---cmartin@rndcs13---/proj/xa5a/REV/ercrun/periphDLLCAP
Thu May 31 13:09:42 MDT 2001---t85a@rndcs9---/proj/t85a/REV/ercrun/t85aQ
Fri Jun  1 09:13:26 MDT 2001---t85a@rndcs18---/proj/t85a/REV/ercrun/qsdrv_p10
Fri Jun  1 15:45:27 MDT 2001---vwong@rndcs12---/proj/y25a/REV/ercrun/padsup
Fri Jun  1 15:58:16 MDT 2001---vwong@rndcs28---/proj/y25a/REV/ercrun/padsup
Mon Jun  4 10:01:24 MDT 2001---ksdyer@rndcs10---/proj/t85a/REV/ercrun/padsup
Mon Jun  4 10:13:19 MDT 2001---ksdyer@rndcs13---/proj/t85a/REV/ercrun/center_mid
Mon Jun  4 10:14:16 MDT 2001---ksdyer@rndcs19---/proj/t85a/REV/ercrun/center_left
Mon Jun  4 10:14:31 MDT 2001---ksdyer@rndcs16---/proj/t85a/REV/ercrun/center_right
Mon Jun  4 10:18:05 MDT 2001---ksdyer@rndcs7---/proj/t85a/REV/ercrun/padsdn
Wed Jun  6 09:45:14 MDT 2001---jsher@rndcs8---/proj/s93a/REV/ercrun/CENTER
Wed Jun  6 09:48:54 MDT 2001---jsher@rndcs10---/proj/s93a/REV/ercrun/LEFT
Wed Jun  6 09:49:25 MDT 2001---jsher@rndcs6---/proj/s93a/REV/ercrun/RIGHT
Wed Jun  6 09:49:36 MDT 2001---jsher@rndcs28---/proj/s93a/REV/ercrun/TOP
Wed Jun  6 09:50:02 MDT 2001---jsher@rndcs16---/proj/s93a/REV/ercrun/BOTTOM
Wed Jun  6 14:47:05 MDT 2001---vwong@rndcs15---/proj/y25a/REV/ercrun/y25aQ
Thu Jun  7 08:37:39 MDT 2001---jsher@rndcs12---/proj/s93a/REV/ercrun/CENTER
Fri Jun  8 09:24:38 MDT 2001---bthompso@rndcs18---/proj/s93a/REV/ercrun/REGBLOCK
Fri Jun  8 09:32:08 MDT 2001---bthompso@rndcs7---/proj/s93a/REV/ercrun/REGBLOCK
Fri Jun  8 09:43:47 MDT 2001---bthompso@rndcs7---/proj/s93a/REV/ercrun/REGBLOCK
Fri Jun  8 10:00:02 MDT 2001---bthompso@rndcs7---/proj/s93a/REV/ercrun/REGBLOCK
Fri Jun  8 10:01:51 MDT 2001---bthompso@rndcs9---/proj/s93a/REV/ercrun/REGBLOCK
Fri Jun  8 10:46:39 MDT 2001---bthompso@rndcs28---/proj/s93a/REV/ercrun/REGBLOCK
Fri Jun  8 11:52:57 MDT 2001---bthompso@rndcs12---/proj/s93a/REV/ercrun/REGBLOCK
Fri Jun  8 11:57:55 MDT 2001---jsher@rndcs10---/proj/s93a/REV/ercrun/TOP
Fri Jun  8 11:58:01 MDT 2001---jsher@rndcs28---/proj/s93a/REV/ercrun/LEFT
Fri Jun  8 11:58:30 MDT 2001---jsher@rndcs10---/proj/s93a/REV/ercrun/BOTTOM
Fri Jun  8 11:58:47 MDT 2001---jsher@rndcs18---/proj/s93a/REV/ercrun/RIGHT
Fri Jun  8 11:59:01 MDT 2001---jsher@rndcs12---/proj/s93a/REV/ercrun/CENTER
Fri Jun  8 12:58:32 MDT 2001---jsher@rndcs7---/proj/s93a/REV/ercrun/periph
Fri Jun  8 14:22:54 MDT 2001---vwong@rndcs14---/proj/y25a/REV/ercrun/center
Fri Jun  8 16:31:03 MDT 2001---jsher@rndcs18---/proj/s93a/REV/ercrun/S93AF
Fri Jun  8 17:23:05 MDT 2001---jsher@rndcs15---/proj/s93a/REV/ercrun/periph
Fri Jun  8 17:39:49 MDT 2001---jsher@rndcs18---/proj/s93a/REV/ercrun/S93AF
Sat Jun  9 07:53:39 MDT 2001---bthompso@rndcs6---/proj/s93a/REV/ercrun/S93AF
Sat Jun  9 11:39:08 MDT 2001---jsher@rndcs6---/proj/s93a/REV/ercrun/S93AF
Tue Jun 12 17:49:57 MDT 2001---vwong@rndcs15---/proj/y25a/REV/ercrun/center
Tue Jun 12 18:16:14 MDT 2001---vwong@rndcs14---/proj/y25a/REV/ercrun/center
Wed Jun 13 10:47:08 MDT 2001---vwong@rndcs18---/proj/y25a/REV/ercrun/center
Wed Jun 13 11:06:17 MDT 2001---vwong@rndcs18---/proj/y25a/REV/ercrun/padsup
Wed Jun 13 13:51:32 MDT 2001---vwong@rndcs8---/proj/y25a/REV/ercrun/cgndDrv_we_to_dvc2
Wed Jun 13 15:58:54 MDT 2001---vwong@rndcs28---/proj/y25a/REV/ercrun/padsup
Thu Jun 14 10:54:28 MDT 2001---vwong@rndcs16---/proj/y25a/REV/ercrun/padsup
Thu Jun 14 13:35:07 MDT 2001---vwong@rndcs16---/proj/y25a/REV/ercrun/padsup
Thu Jun 14 13:55:18 MDT 2001---vwong@rndcs18---/proj/y25a/REV/ercrun/padsup
Thu Jun 14 15:34:44 MDT 2001---vwong@rndcs10---/proj/y25a/REV/ercrun/padsup
Thu Jun 14 15:41:20 MDT 2001---vwong@rndcs6---/proj/y25a/REV/ercrun/padsdn
Fri Jun 15 06:34:50 MDT 2001---vwong@rndcs14---/proj/y25a/REV/ercrun/padsdn
Fri Jun 15 06:39:30 MDT 2001---vwong@rndcs9---/proj/y25a/REV/ercrun/vccp_reg
Fri Jun 15 06:44:12 MDT 2001---vwong@rndcs9---/proj/y25a/REV/ercrun/vccp_reg
Fri Jun 15 06:46:22 MDT 2001---vwong@rndcs14---/proj/y25a/REV/ercrun/vccp_core
Fri Jun 15 06:48:22 MDT 2001---vwong@rndcs9---/proj/y25a/REV/ercrun/vccp_osc
Fri Jun 15 06:49:46 MDT 2001---vwong@rndcs7---/proj/y25a/REV/ercrun/pwramp
Fri Jun 15 06:57:26 MDT 2001---vwong@rndcs14---/proj/y25a/REV/ercrun/pwramp
Fri Jun 15 07:00:53 MDT 2001---vwong@rndcs9---/proj/y25a/REV/ercrun/pwrboost
Fri Jun 15 07:09:37 MDT 2001---vwong@rndcs10---/proj/y25a/REV/ercrun/pwrboost
Fri Jun 15 07:11:08 MDT 2001---vwong@rndcs9---/proj/y25a/REV/ercrun/boostamp
Fri Jun 15 07:18:47 MDT 2001---vwong@rndcs8---/proj/y25a/REV/ercrun/boostamp
Fri Jun 15 07:22:28 MDT 2001---vwong@rndcs17---/proj/y25a/REV/ercrun/boostamp
Fri Jun 15 07:23:47 MDT 2001---vwong@rndcs10---/proj/y25a/REV/ercrun/boostamp
Fri Jun 15 08:05:53 MDT 2001---vwong@rndcs7---/proj/y25a/REV/ercrun/stbyamp
Fri Jun 15 08:09:27 MDT 2001---vwong@rndcs10---/proj/y25a/REV/ercrun/stbyamp
Fri Jun 15 08:11:51 MDT 2001---vwong@rndcs17---/proj/y25a/REV/ercrun/stbyamp
Fri Jun 15 08:12:58 MDT 2001---vwong@rndcs19---/proj/y25a/REV/ercrun/stbyamp
Fri Jun 15 08:15:29 MDT 2001---vwong@rndcs18---/proj/y25a/REV/ercrun/triref
Fri Jun 15 08:19:12 MDT 2001---vwong@rndcs7---/proj/y25a/REV/ercrun/regrefn
Fri Jun 15 08:27:42 MDT 2001---vwong@rndcs14---/proj/y25a/REV/ercrun/regrefn
Fri Jun 15 08:40:48 MDT 2001---vwong@rndcs15---/proj/y25a/REV/ercrun/ref
Fri Jun 15 08:46:00 MDT 2001---vwong@rndcs15---/proj/y25a/REV/ercrun/ref
Fri Jun 15 08:48:51 MDT 2001---vwong@rndcs14---/proj/y25a/REV/ercrun/ref
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Fri Jun 15 08:50:13 MDT 2001---vwong@rndcs18---/proj/y25a/REV/ercrun/bandgap
Fri Jun 15 08:55:53 MDT 2001---vwong@rndcs6---/proj/y25a/REV/ercrun/triref
Fri Jun 15 09:09:22 MDT 2001---vwong@rndcs7---/proj/y25a/REV/ercrun/triref
Fri Jun 15 09:13:47 MDT 2001---vwong@rndcs7---/proj/y25a/REV/ercrun/powerup
Fri Jun 15 09:15:33 MDT 2001---vwong@rndcs7---/proj/y25a/REV/ercrun/powerup2
Fri Jun 15 09:21:44 MDT 2001---vwong@rndcs18---/proj/y25a/REV/ercrun/powerup2
Fri Jun 15 09:23:37 MDT 2001---vwong@rndcs15---/proj/y25a/REV/ercrun/vbb_pump
Fri Jun 15 09:28:48 MDT 2001---vwong@rndcs17---/proj/y25a/REV/ercrun/y95a_vbbcore
Fri Jun 15 09:29:56 MDT 2001---vwong@rndcs18---/proj/y25a/REV/ercrun/y95a_vbbcore
Fri Jun 15 09:39:48 MDT 2001---vwong@rndcs9---/proj/y25a/REV/ercrun/y95a_vbbcore
Fri Jun 15 09:41:28 MDT 2001---vwong@rndcs10---/proj/y25a/REV/ercrun/y95a_vbbcore
Fri Jun 15 09:41:34 MDT 2001---vwong@rndcs13---/proj/y25a/REV/ercrun/y95a_vbbcore
Fri Jun 15 09:41:50 MDT 2001---vwong@rndcs15---/proj/y25a/REV/ercrun/y95a_vbbcore
Fri Jun 15 09:45:29 MDT 2001---vwong@rndcs16---/proj/y25a/REV/ercrun/y95a_vbbcore
Fri Jun 15 09:50:15 MDT 2001---vwong@rndcs14---/proj/y25a/REV/ercrun/y95a_vbbcore
Fri Jun 15 09:53:32 MDT 2001---vwong@rndcs12---/proj/y25a/REV/ercrun/y95a_vbbcore
Fri Jun 15 09:54:49 MDT 2001---vwong@rndcs10---/proj/y25a/REV/ercrun/vbb_pump
Fri Jun 15 09:57:36 MDT 2001---vwong@rndcs12---/proj/y25a/REV/ercrun/padsdn
Fri Jun 15 10:10:08 MDT 2001---vwong@rndcs13---/proj/y25a/REV/ercrun/padsdn
Fri Jun 15 10:16:52 MDT 2001---vwong@rndcs12---/proj/y25a/REV/ercrun/p_pumps
Fri Jun 15 13:37:34 MDT 2001---vwong@rndcs13---/proj/y25a/REV/ercrun/y25aQ
Mon Jun 18 13:11:26 MDT 2001---vwong@rndcs14---/proj/y25a/REV/ercrun/y25aQ
Tue Jun 19 10:39:16 MDT 2001---jsher@rndcs12---/proj/s93a/REV/ercrun/S93AF
Thu Jun 21 07:50:56 MDT 2001---vwong@rndcs9---/proj/y25a/REV/ercrun/y25aQ
Fri Jun 22 13:27:50 MDT 2001---y15b@rndcs28---/proj/y15b/REV/ercrun/y95a_vbbcore
Thu Jun 28 11:52:54 MDT 2001---russphil@rndcs6---/proj/xa5a/REV/ercrun/dvc2Top
Mon Jul  2 09:24:23 MDT 2001---gsingh@rndcs10---/proj/u96a/REV/ercrun/Fullchip
Tue Jul  3 09:40:06 MDT 2001---vwong@rndcs9---/proj/y25a/REV/ercrun/y25aQ
Tue Jul  3 11:21:25 MDT 2001---vwong@rndcs17---/proj/y25a/REV/ercrun/we_pass
Tue Jul  3 11:27:42 MDT 2001---vwong@rndcs18---/proj/y25a/REV/ercrun/dqboot
Tue Jul  3 11:30:15 MDT 2001---vwong@rndcs15---/proj/y25a/REV/ercrun/dqboot
Tue Jul  3 11:37:00 MDT 2001---vwong@rndcs17---/proj/y25a/REV/ercrun/dqboot
Tue Jul  3 11:44:36 MDT 2001---vwong@rndcs9---/proj/y25a/REV/ercrun/isodrv
Tue Jul  3 11:45:51 MDT 2001---vwong@rndcs8---/proj/y25a/REV/ercrun/isodrv
Tue Jul  3 11:48:51 MDT 2001---vwong@rndcs29---/proj/y25a/REV/ercrun/vccp_inv
Tue Jul  3 11:49:53 MDT 2001---vwong@rndcs17---/proj/y25a/REV/ercrun/vccp_inv
Tue Jul  3 12:16:26 MDT 2001---vwong@rndcs15---/proj/y25a/REV/ercrun/vccp_p_core_t17a
Tue Jul  3 12:28:31 MDT 2001---vwong@rndcs7---/proj/y25a/REV/ercrun/y25aQ
Thu Jul  5 13:51:33 MDT 2001---gsingh@rndcs28---/proj/u96a/REV/ercrun/u96af
Fri Jul  6 08:28:56 MDT 2001---gsingh@rndcs14---/proj/u96a/REV/ercrun/u96af
Fri Jul  6 16:19:12 MDT 2001---gsingh@rndcs10---/proj/u96a/REV/ercrun/u96af
Mon Jul  9 10:22:38 MDT 2001---gsingh@rndcs12---/proj/u96a/REV/ercrun/u96af
Mon Jul 16 13:06:42 MDT 2001---t85a@rndcs19---/proj/t85a/REV/ercrun/t85aQ
Wed Jul 18 15:44:31 MDT 2001---dgans@rndcs13---/proj/s10w/REV/ercrun/DQMODULE
Wed Jul 18 15:45:39 MDT 2001---dgans@rndcs22---/proj/s10w/REV/ercrun/DQMODULE
Wed Jul 18 17:42:04 MDT 2001---dgans@rndcs12---/proj/s10w/REV/ercrun/DQMODULE
Wed Jul 25 08:52:57 MDT 2001---tmerritt@rndcs9---/proj/t17a/REV/ercrun/center
Wed Jul 25 15:36:11 MDT 2001---tmerritt@rndcs16---/proj/t17a/REV/ercrun/inputpad
Wed Jul 25 15:43:27 MDT 2001---tmerritt@rndcs21---/proj/t17a/REV/ercrun/inputpad
Wed Jul 25 16:05:35 MDT 2001---tmerritt@rndcs26---/proj/t17a/REV/ercrun/inputpad_A12
Thu Jul 26 06:06:12 MDT 2001---cingalls@rndcs12---/proj/t17a/REV/ercrun/regulator
Thu Jul 26 08:05:28 MDT 2001---cingalls@rndcs18---/proj/t17a/REV/ercrun/GlbFuseTier
Thu Jul 26 09:27:33 MDT 2001---cingalls@rndcs19---/proj/t17a/REV/ercrun/GlbFuseTier
Thu Jul 26 09:47:49 MDT 2001---cingalls@rndcs26---/proj/t17a/REV/ercrun/regulator
Thu Jul 26 10:29:37 MDT 2001---cingalls@rndcs17---/proj/t17a/REV/ercrun/regulator
Thu Jul 26 10:57:09 MDT 2001---cingalls@rndcs23---/proj/t17a/REV/ercrun/cgndCore
Thu Jul 26 12:58:03 MDT 2001---graad@rndcs13---/proj/t17a/REV/ercrun/pads_all
Thu Jul 26 14:18:15 MDT 2001---cingalls@rndcs28---/proj/t17a/REV/ercrun/GlbFuseTier
Thu Jul 26 14:45:26 MDT 2001---sbatra@rndcs16---/proj/t17a/REV/ercrun/dqdrv
Thu Jul 26 14:47:05 MDT 2001---sbatra@rndcs15---/proj/t17a/REV/ercrun/dqdrv
Thu Jul 26 14:51:25 MDT 2001---graad@rndcs21---/proj/t17a/REV/ercrun/data_mask
Thu Jul 26 16:34:00 MDT 2001---graad@rndcs21---/proj/t17a/REV/ercrun/data_mask
Fri Jul 27 07:54:14 MDT 2001---graad@rndcs26---/proj/t17a/REV/ercrun/data_mask
Fri Jul 27 07:55:15 MDT 2001---tmerritt@rndcs23---/proj/t17a/REV/ercrun/center
Fri Jul 27 08:29:14 MDT 2001---graad@rndcs16---/proj/t17a/REV/ercrun/data_mask
Fri Jul 27 08:53:41 MDT 2001---graad@rndcs7---/proj/t17a/REV/ercrun/data_mask
Fri Jul 27 09:15:18 MDT 2001---graad@rndcs12---/proj/t17a/REV/ercrun/data_mask
Mon Jul 30 06:07:31 MDT 2001---cingalls@rndcs24---/proj/t17a/REV/ercrun/center
Mon Jul 30 08:15:16 MDT 2001---tmerritt@rndcs21---/proj/t17a/REV/ercrun/throat
Mon Jul 30 08:21:21 MDT 2001---tmerritt@rndcs26---/proj/t17a/REV/ercrun/array32meg
Mon Jul 30 09:04:09 MDT 2001---tmerritt@rndcs19---/proj/t17a/REV/ercrun/array256meg
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Mon Jul 30 09:04:59 MDT 2001---russphil@rndcs24---/proj/xa5a/REV/ercrun/dvc2Top_test
Mon Jul 30 13:56:38 MDT 2001---tmerritt@rndcs25---/proj/t17a/REV/ercrun/center
Tue Jul 31 11:09:23 MDT 2001---graad@rndcs17---/proj/t17a/REV/ercrun/center
Tue Jul 31 11:36:28 MDT 2001---cmartin@rndcs21---/proj/xa5a/REV/ercrun/periphDLLCAP
Tue Jul 31 12:06:18 MDT 2001---cmartin@rndcs26---/proj/xa5a/REV/ercrun/periphPOINT
Tue Jul 31 12:29:34 MDT 2001---cmartin@rndcs14---/proj/xa5a/REV/ercrun/periphESD
Tue Jul 31 14:04:22 MDT 2001---cmartin@rndcs14---/proj/xa5a/REV/ercrun/periphSLEW
Tue Jul 31 15:29:06 MDT 2001---graad@rndcs23---/proj/t17a/REV/ercrun/center
Tue Jul 31 17:20:32 MDT 2001---tmerritt@rndcs28---/proj/t17a/REV/ercrun/t17af
Tue Jul 31 17:25:00 MDT 2001---graad@rndcs14---/proj/t17a/REV/ercrun/center
Wed Aug  1 09:16:34 MDT 2001---tmerritt@rndcs13---/proj/t17a/REV/ercrun/dq_info_out
Wed Aug  1 09:13:59 MDT 2001---tmerritt@rndcs20---/proj/t17a/REV/ercrun/dq_info_out
Wed Aug  1 09:31:31 MDT 2001---tmerritt@rndcs10---/proj/t17a/REV/ercrun/DLL_clk_diff
Wed Aug  1 13:47:55 MDT 2001---tmerritt@rndcs24---/proj/t17a/REV/ercrun/input_logic2
Wed Aug  1 14:10:16 MDT 2001---tmerritt@rndcs12---/proj/t17a/REV/ercrun/pads_all
Wed Aug  1 15:21:51 MDT 2001---tmerritt@rndcs22---/proj/t17a/REV/ercrun/center
Thu Aug  2 11:00:48 MDT 2001---tmerritt@rndcs19---/proj/t17a/REV/ercrun/center
Thu Aug  2 18:07:04 MDT 2001---tmerritt@rndcs25---/proj/t17a/REV/ercrun/center
Thu Aug  2 18:07:05 MDT 2001---tmerritt@rndcs21---/proj/t17a/REV/ercrun/center
Fri Aug  3 06:14:08 MDT 2001---cingalls@rndcs12---/proj/t17a/REV/ercrun/GlbFuseTier
Fri Aug  3 07:37:48 MDT 2001---cingalls@rndcs17---/proj/t17a/REV/ercrun/regulator
Fri Aug  3 08:19:56 MDT 2001---cingalls@rndcs26---/proj/t17a/REV/ercrun/regulator
Fri Aug  3 08:29:23 MDT 2001---cingalls@rndcs26---/proj/t17a/REV/ercrun/center
Fri Aug  3 10:47:52 MDT 2001---tmerritt@rndcs14---/proj/t17a/REV/ercrun/center
Mon Aug  6 10:08:44 MDT 2001---tmerritt@rndcs9---/proj/t17a/REV/ercrun/t17af
Mon Aug  6 13:39:40 MDT 2001---dgans@rndcs21---/proj/s10w/REV/ercrun/RIGHT
Mon Aug  6 14:31:17 MDT 2001---dgans@rndcs16---/proj/s10w/REV/ercrun/RIGHT
Wed Aug  8 09:19:27 MDT 2001---kkalpakj@rndcs17---/proj/s97a/REV/ercrun/S97AF
Wed Aug  8 14:20:07 MDT 2001---kkalpakj@rndcs19---/proj/s97a/REV/ercrun/S97AF
Wed Aug  8 15:57:52 MDT 2001---kkalpakj@rndcs9---/proj/s97a/REV/ercrun/S97AF
Wed Aug  8 17:27:10 MDT 2001---kkalpakj@rndcs28---/proj/s97a/REV/ercrun/S97AF
Thu Aug  9 09:27:48 MDT 2001---huyvo@rndcs12---/proj/z93a/REV_DRAM/ercrun/M8128IOcore_int
Thu Aug  9 10:00:12 MDT 2001---huyvo@rndcs16---/proj/z93a/REV_DRAM/ercrun/M8128IOcore_int
Thu Aug  9 12:11:10 MDT 2001---kkalpakj@rndcs23---/proj/s97a/REV/ercrun/S97AF
Thu Aug  9 16:24:26 MDT 2001---kkalpakj@rndcs19---/proj/s97a/REV/ercrun/S97AF
Fri Aug 10 10:56:17 MDT 2001---kkalpakj@rndcs12---/proj/s97a/REV/ercrun/S97AF
Mon Aug 13 10:43:12 MDT 2001---huyvo@rndcs16---/proj/z93a/REV_DRAM/ercrun/M8128IOcore_int_TI
Mon Aug 20 16:27:15 MDT 2001---jcullum@rndcs28---/proj/rd20/REV/ercrun/ltdrvr65_new
Tue Aug 21 09:43:55 MDT 2001---kkalpakj@rndcs10---/proj/s97a/REV/ercrun/TMVCCA_pass
Tue Aug 21 09:55:19 MDT 2001---kkalpakj@rndcs14---/proj/s97a/REV/ercrun/TMVCCA_pass
Tue Aug 21 10:07:09 MDT 2001---kkalpakj@rndcs14---/proj/s97a/REV/ercrun/S97AF
Tue Aug 21 10:08:18 MDT 2001---t85a@rndcs28---/proj/t85a/REV/ercrun/t85aQ
Tue Aug 21 10:49:58 MDT 2001---kkalpakj@rndcs14---/proj/s97a/REV/ercrun/S97AF
Tue Aug 21 11:43:14 MDT 2001---bthompso@rndcs7---/proj/s93a/REV/ercrun/S93AF
Tue Aug 21 14:24:39 MDT 2001---dgans@rndcs12---/proj/s10w/REV/ercrun/CENTER_left
Tue Aug 21 14:37:58 MDT 2001---dgans@rndcs17---/proj/s10w/REV/ercrun/CENTER_left
Tue Aug 21 14:55:31 MDT 2001---dgans@rndcs16---/proj/s10w/REV/ercrun/CENTER_left
Tue Aug 21 15:08:25 MDT 2001---dgans@rndcs17---/proj/s10w/REV/ercrun/CENTER_right
Tue Aug 21 15:18:42 MDT 2001---dgans@rndcs19---/proj/s10w/REV/ercrun/CENTER_right
Tue Aug 21 15:23:43 MDT 2001---dgans@rndcs10---/proj/s10w/REV/ercrun/CENTER_right
Tue Aug 21 16:33:06 MDT 2001---dgans@rndcs17---/proj/s10w/REV/ercrun/CENTER_right
Tue Aug 21 16:40:30 MDT 2001---dgans@rndcs17---/proj/s10w/REV/ercrun/CENTER_right
Tue Aug 21 16:56:37 MDT 2001---dgans@rndcs10---/proj/s10w/REV/ercrun/TOP
Tue Aug 21 17:32:00 MDT 2001---dgans@rndcs15---/proj/s10w/REV/ercrun/BOTTOM
Tue Aug 21 17:43:44 MDT 2001---dgans@rndcs13---/proj/s10w/REV/ercrun/BOTTOM
Wed Aug 22 09:00:26 MDT 2001---dgans@rndcs18---/proj/s10w/REV/ercrun/RIGHT
Wed Aug 22 09:20:48 MDT 2001---dgans@rndcs28---/proj/s10w/REV/ercrun/RIGHT
Wed Aug 22 09:27:25 MDT 2001---dgans@rndcs14---/proj/s10w/REV/ercrun/LEFT
Wed Aug 22 10:09:37 MDT 2001---dgans@rndcs19---/proj/s10w/REV/ercrun/LEFT
Wed Aug 22 10:39:24 MDT 2001---dgans@rndcs19---/proj/s10w/REV/ercrun/LEFT
Wed Aug 22 12:39:01 MDT 2001---dgans@rndcs7---/proj/s10w/REV/ercrun/S10WF
Wed Aug 22 13:50:48 MDT 2001---dgans@rndcs17---/proj/s10w/REV/ercrun/S10WF
Mon Aug 27 16:14:40 MDT 2001---huyvo@rndcs8---/proj/z93a/REV_DRAM/ercrun/M8128IOcore_int_TI
Mon Aug 27 17:08:08 MDT 2001---huyvo@rndcs10---/proj/z93a/REV_DRAM/ercrun/M8128IOcore_int_TI
Tue Aug 28 11:03:09 MDT 2001---huyvo@rndcs26---/proj/z93a/REV_DRAM/ercrun/M8128IOcore_int_TI
Tue Aug 28 17:38:19 MDT 2001---huyvo@rndcs10---/proj/z93a/REV_DRAM/ercrun/M8128IOcore_int_TI
Thu Aug 30 08:31:36 MDT 2001---huyvo@rndcs16---/proj/z93a/REV_DRAM/ercrun/M8128IOcore_int_TI
Fri Sep  7 17:28:00 MDT 2001---dnorvall@rndcs24---/proj/l10a/REV/ercrun/TAG2sq_Q
Fri Sep  7 17:29:02 MDT 2001---dnorvall@rndcs9---/proj/l10a/REV/ercrun/TAG3sq_Q
Fri Sep 14 08:44:53 MDT 2001---kkalpakj@rndcs25---/proj/s97a/REV/ercrun/S97AF
Mon Sep 17 09:48:14 MDT 2001---tmerritt@rndcs25---/proj/t17a/REV/ercrun/t17af
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Mon Sep 17 10:15:37 MDT 2001---ksdyer@rndcs7---/proj/t17a/REV/ercrun/center
Mon Sep 17 13:42:03 MDT 2001---tmerritt@rndcs7---/proj/t17a/REV/ercrun/t17af
Wed Sep 19 09:05:15 MDT 2001---kkalpakj@rndcs13---/proj/s97a/REV/ercrun/S97AF
Wed Sep 19 09:12:44 MDT 2001---tmerritt@rndcs24---/proj/t17a/REV/ercrun/t17af
Wed Sep 19 15:06:16 MDT 2001---kkalpakj@rndcs17---/proj/s97a/REV/ercrun/S97AF
Thu Sep 20 09:38:55 MDT 2001---kkalpakj@rndcs23---/proj/s97a/REV/ercrun/S97AF
Thu Sep 20 12:46:30 MDT 2001---kkalpakj@rndcs22---/proj/s97a/REV/ercrun/S97AF
Thu Sep 20 23:03:31 MDT 2001---kkalpakj@rndcs10---/proj/s97a/REV/ercrun/S97AF
Fri Sep 21 11:33:36 MDT 2001---kkalpakj@rndcs28---/proj/s97a/REV/ercrun/S97AF
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